



Product Service

ATTACHMENT TO CERTIFICATE

No. Z10 17 01 84071 018

Nomenclature of Product type TMS570LSx

Orderable part number	Part number
TMS5700914APGEEQ1	TMS570LS0914
TMS5700914APZQQ1	TMS570LS0914
TMS5700714APGEEQ1	TMS570LS0714
TMS5700714APZQQ1	TMS570LS0714
TMS5700914APGEEQ1R	TMS570LS0914
TMS5700914APZQQ1R	TMS570LS0914
TMS5700714APGEEQ1R	TMS570LS0714
TMS5700714APZQQ1R	TMS570LS0714



Report
on the
Certificate
Z10 17 01 84071 018

Manufacturer:

Texas Instruments Incorporated
13905 University Blvd.
Sugar Land TX 77479
USA

Report no. TH90323C
Revision 1.1 of 2017-03-27

Test Body
TÜV SÜD Rail GmbH
Generic Safety Systems
D-80339 Munich

Certification Body
TÜV SÜD Product Service GmbH

D-80339 Munich

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Revision

Version	Status	Date	Author	Changed chapters	Reason of change
1.0	final	2017-01-27	Axel Köhnen	Initial	
1.1	final	2017-03-27	Axel Köhnen	Chapter 2.2	Revision corrected from B to A

Table 1: Revision



1 Purpose and Scope

In July 2016 the company Texas Instruments Incorporated assigned TÜV SÜD for testing and certification the Safety MCU TMS570LSx according to ASIL D of ISO 26262:2011 and SIL 3 of IEC 61508:2010. The project number related to these activities was: 717513322. The report on the certificate gives an overview related to the results of the certification process and the general safety relevant conditions and restrictions related to the use of the Safety MCU TMS570LSx.

2 Target of evaluation

2.1 Description

The target of evaluation is a generic safety microcontroller device based on the Hercules TMS570LSx and RM4x platform architecture. The assessment is based on a tailored safety lifecycle for safety elements out of context (SEooC) according to ISO 26262:2011 and compliant items according to IEC 61508:2010.

2.2 Identification

This report is valid for the silicon revision A. The models covered by the certificate are listed in the following table:

Orderable Part Number	Part Number
TMS5700914APGEQQ1	TMS570LS0914
TMS5700914APZQQ1	TMS570LS0914
TMS5700714APGEQQ1	TMS570LS0714
TMS5700714APZQQ1	TMS570LS0714
TMS5700914APGEQQ1R	TMS570LS0914
TMS5700914APZQQ1R	TMS570LS0914
TMS5700714APGEQQ1R	TMS570LS0714
TMS5700714APZQQ1R	TMS570LS0714

Table 1: Identification



3 Certification Requirements

3.1 Basis of Certification

The certification of the Safety MCU was performed according to the standards listed in clause 3.3 of this document. The basis of the certification was the successful completion of the following test segments:

- I. Functional Safety
 - Functional Safety management (FSM) and safety life-cycle
 - Avoidance of systematic faults
 - Hardware safety requirements (assumptions of use)
 - Analysis of the device structure (IP FMEAs, DFA)
 - Quantitative analysis of the hardware (FMEDA)
 - Fault injection and simulation
 - Hardware functional test and design verification
 - Hardware qualification
 - Development tool qualification
- II. Safety information in the product documentation (Safety manual, operating instructions)
- III. Product-related Quality Management in manufacturing

Certification is dependent on successful completion of all above listed test segments. The testing follows the basic certification scheme for Safety Components of TÜV SÜD Rail GmbH.

3.2 Certification Documentation

The detailed technical evaluation is documented in the technical report N° TH90322T.



3.3 Functional Safety

The testing for Functional Safety is to be performed using the following standards:

No.	Standard	Title
[N1]	IEC 61508-1: 2010 (SIL 3)	Functional safety of electrical/electronic/programmable electronic safety-related systems Part 1: General requirements
[N2]	IEC 61508-2: 2010 (SIL 3)	Functional safety of electrical/electronic/programmable electronic safety-related systems Part 2: Requirements for electrical/electronic/ programmable electronic safety-related systems
[N3]	ISO 26262-2:2011 (ASIL D)	Road vehicles — Functional safety — Part 2: Management of functional safety
[N4]	ISO 26262-5:2011 (ASIL D)	Road vehicles — Functional safety — Part 5: Product development at the hardware level

Table 2: Functional Safety



4 Results

4.1 Functional Safety

The tests performed and quality assurance measures implemented by the manufacturer have shown that the Safety MCU complies with the tailored testing criteria specified in clause 3.3. The Safety MCU provides different safety features implemented on-chip and requires additional safety mechanism to be implemented by the system integrator as software measures and external measures on system level. By using the different safety mechanisms the MCU can be used to support safety functions up to SIL 3 according to IEC 61508:2010 and ASIL D according to ISO 26262:2011.

4.2 General Conditions and Restrictions

The use of the Safety MCU shall comply with the current version of the safety relevant parts of the user documentation. The following list describes the main conditions and restrictions of use:

- The guidelines and requirements specified in the user documentation shall be followed. Especially the requirements of the system integration section of the Safety manual have to be regarded.
- The impact on the overall safety concept and the safety function has to be well understood and analysed if a safety mechanism described in the Safety manual is not used.
- All safety mechanism implemented by the system integrator have to be developed and verified according to the targeted safety standards
- All specific required characteristics and behaviour of the Safety MCU required by the final safety function have to be developed and verified according to the targeted safety standards. This includes also timing aspects like reaction times, test intervals or test execution times.
- The system integrator has to make sure that the conditions and restrictions defined in the documentation of the Safety MCU are understood and followed.



5 Certificate Number

This report defines conditions and restrictions required for the application of the Safety MCU to the certificate:

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Munich, 2017-03-27

TÜV SÜD Rail GmbH
Embedded Systems


Guido Neumann
(Technical Certifier)

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