TLVH431、TLVH432 低電圧、高精度、可変シャント・レギュレータ

1 特長

- 最小 1.24V で動作
- 25℃でのリファレンス電圧の公差
  - 0.5% (B グレード)
  - 1% (A グレード)
  - 1.5% (標準グレード)
- 可変出力電圧、V_O = V_{REF}～18V
- 広い動作カソード電流範囲:
  100µA～70mA
- 出力インピーダンス:0.25Ω (標準値)
- -40°C～+125°C の仕様
- TLVH432
  には、SOT-23-3 および SOT-89 パッケージ用の代替ピン配置があります
- 超小型の SC-70 パッケージで、SOT-23-3 よりも 40% 小さい占有面積

2 アプリケーション

- データ・コンバータ用の可変電圧リファレンス
- フライバック SMPS の 2 次側レギュレーション
- リーク電流の少ないツェナーの代替品
- 電源レールの電圧監視
- リファレンス電圧内蔵のコンパレータ

3 概要

TLVH431 および TLVH432 デバイスは低電圧、3 端子の可変電圧リファレンスで、該当する産業用および商業用温度範囲全体にわたって規定の熱的安定性を維持します。出力電圧は、2 つの外付け抵抗により V_{REF} (1.24V) と 18V の間の任意の値に設定できます (「図 7-2」セクションを参照)。これらのデバイスは、広く使用されている TL431 および TL1431 シャント・レギュレータのリファレンス電圧よりも低い電圧 (1.24V) で動作します。

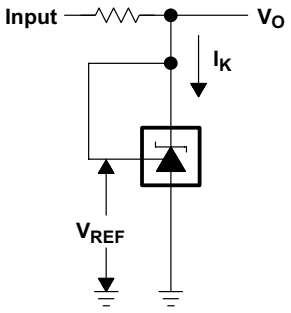
オプトカプラとともに使用すると、TLVH431 および TLVH432 は 3V～3.3V のスイッチング・モード電源用の絶縁帰還回路における理想的な電圧リファレンスになります。これらのデバイスの出力インピーダンスは 0.25Ω (標準値) です。アクティブ出力回路により、非常に鋭いターンオン特性を持つことから、TLVH431 および TLVH432 はオンボード・レギュレーションや可変電源など多くのアプリケーションにおいて、低電圧ツェナー・ダイオードの非常に優れた代替品となります。

TLVH432 デバイス
は TLVH431 デバイスと同じですが、3 ピン SOT-23 および SOT-89 パッケージ用の異なるピン配置で供給されます。

デバイス情報

部品番号	パッケージ(1)	本体サイズ (公称)
TLVH43xxDBV	SOT-23 (5)	2.90mm × 1.60mm
TLVH43xxDBZ	SOT-23 (3)	2.92mm × 1.30mm
TLVH43xxDCK	SC70 (6)	2.00mm × 1.25mm
TLVH43xxLP	TO-92 (3)	4.30mm × 4.30mm
TLVH43xxPK	SOT-89 (3)	4.50mm × 2.50mm

(1) 利用可能なすべてのパッケージについては、このデータシートの末尾にある注文情報を参照してください。



概略回路図

英語版の TI 製品についての情報を翻訳したこの資料は、製品の概要を確認する目的で便宜的に提供しているものです。該当する正式な英語版の最新情報は、www.ti.com で閲覧でき、その内容が常に優先されます。TI では翻訳の正確性および妥当性につきましては一切保証いたしません。実際の設計などの前には、必ず最新版の英語版をご参照くださいますようお願いいたします。

Table of Contents

1 特長 ... 1
2 アプリケーション ... 1
3 概要 ... 1
4 Revision History ... 2
5 Pin Configuration and Functions ... 3
Pin Functions ... 4
6 Specifications ... 5
6.1 Absolute Maximum Ratings ... 5
6.2 ESD Ratings ... 5
6.3 Recommended Operating Conditions ... 5
6.4 Thermal Information ... 5
6.5 TLVH43x Electrical Characteristics ... 6
6.6 TLVH43xA Electrical Characteristics ... 7
6.7 TLVH43xB Electrical Characteristics ... 8
6.8 Typical Characteristics ... 8
7 Parameter Measurement Information ... 12
8 Detailed Description ... 13
8.1 Overview ... 13
8.2 Functional Block Diagram ... 13
8.3 Feature Description ... 14
8.4 Device Functional Modes ... 15
9 Applications and Implementation ... 16
9.1 Application Information ... 16
9.2 Typical Applications ... 17
10 Power Supply Recommendations ... 21
11 Layout ... 22
11.1 Layout Guidelines ... 22
11.2 Layout Example ... 22
12 Device and Documentation Support ... 23
12.1 Documentation Support ... 23
12.2 ドキュメントの更新通知を受け取る方法 ... 23
12.3 サポート・リソース ... 23
12.4 Related Links ... 23
12.5 Trademarks ... 23
12.6 静電気放電に関する注意事項 ... 23
12.7 用語集 ... 23
13 Mechanical, Packaging, and Orderable Information ... 23

4 Revision History

資料番号末尾の英字は改訂を表しています。その改訂履歴は英語版に準じています。

Changes from Revision L (April 2020) to Revision M (January 2023) — **Page**

- 文書全体にわたって表、図、相互参照の採番方法を更新 ... 1
- 「製品情報」表を修正 ... 1

Changes from Revision K (September 2016) to Revision L (April 2020) — **Page**

- TI.com のアプリケーションへのリンクを追加 ... 1
- Changed Thermal Information ... 5
- Changed load capacitance value to better reflect the device behavior ... 19

Changes from Revision J (January 2015) to Revision K (September 2016) — **Page**

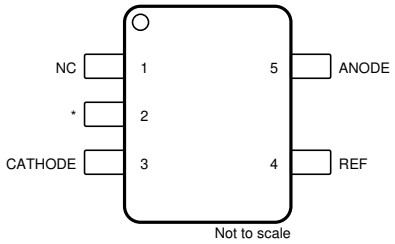
- データシートのタイトルを変更 ... 1
- Updated pinout images and *Pin Functions* table ... 3
- Deleted D package from *Pin Functions* table ... 4

Changes from Revision I (September 2009) to Revision J (January 2015) — **Page**

- 「アプリケーション」セクション、「製品情報」表、「ピンの機能」表、「ESD 定格」表、「熱に関する情報」表、「代表的特性」セクション、「機能説明」セクション、「デバイスの機能モード」セクション、「アプリケーションと実装」セクション、「電源に関する推奨事項」セクション、「レイアウト」セクション、「デバイスおよびドキュメントのサポート」セクション、「メカニカル、パッケージ、および注文情報」セクションを追加 ... 1
- 「注文情報」表を削除 ... 1

Copyright © 2023 Texas Instruments Incorporated

5 Pin Configuration and Functions



NC – No internal connection

* Pin 2 is attached to Substrate and must be connected to ANODE or left open.

図 5-1. TLVH431 DBV Package 5-Pin SOT-23 Top View

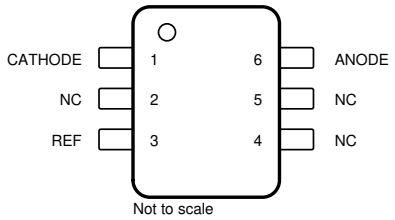


図 5-2. TLVH431 DCK Package 6-Pin SC70 Top View

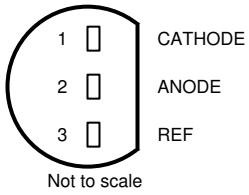


図 5-3. TLVH431 LP Package 3-Pin TO-92 Top View

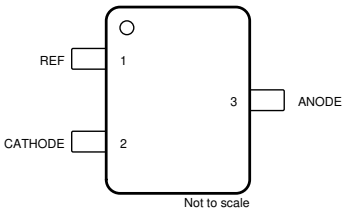


図 5-4. TLVH431 DBZ Package 3-Pin SOT-23 Top View

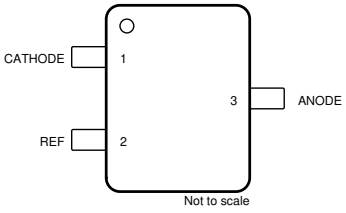


図 5-5. TLVH432 DBZ Package 3-Pin SOT-23 Top View

Copyright © 2023 Texas Instruments Incorporated

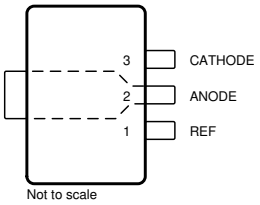


図 5-6. TLVH431 PK Package 3-Pin SOT-89 Top View

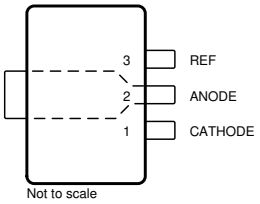


図 5-7. TLVH432 PK Package 3-Pin SOT-89 Top View

Pin Functions

PIN								TYPE	DESCRIPTION
NAME	TLVH431					TLVH432			
	DBZ	DBV	LP	DCK	PK	DBZ	PK		
CATHODE	2	3	1	1	3	1	1	I/O	Shunt Current/Voltage input
REF	1	4	3	3	1	2	3	I	Threshold relative to common anode
ANODE	3	5	2	6	2	3	2	O	Common pin, normally connected to ground
NC	—	1	—	2, 4, 5	—	—	—	I	No Internal Connection
*	—	2	—	—	—	—	—	I	Substrate Connection

Copyright © 2023 Texas Instruments Incorporated

6 Specifications

6.1 Absolute Maximum Ratings

over operating free-air temperature range (unless otherwise noted)(1)

		MIN	MAX	UNIT
V_{KA}	Cathode voltage(2)		20	V
I_K	Cathode current	–25	80	mA
I_{ref}	Reference current	–0.05	3	mA
T_J	Operating virtual junction temperature		150	°C
T_{stg}	Storage temperature	–65	150	°C

(1) Stresses beyond those listed under *Absolute Maximum Ratings* may cause permanent damage to the device. These are stress ratings only, which do not imply functional operation of the device at these or any other conditions beyond those indicated under *Recommended Operating Conditions*. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.
(2) Voltage values are with respect to the anode terminal, unless otherwise noted.

6.2 ESD Ratings

			VALUE	UNIT
$V_{(ESD)}$	Electrostatic discharge	Human body model (HBM), per ANSI/ESDA/JEDEC JS-001(1)	±2000	V
		Charged device model (CDM), per JEDEC specification JESD22-C101(2)	±1000	

(1) JEDEC document JEP155 states that 500-V HBM allows safe manufacturing with a standard ESD control process.
(2) JEDEC document JEP157 states that 250-V CDM allows safe manufacturing with a standard ESD control process.

6.3 Recommended Operating Conditions

See(1)

			MIN	MAX	UNIT
V_{KA}	Cathode voltage		V_{REF}	18	V
I_K	Cathode current (continuous)		0.1	70	mA
T_A	Operating free-air temperature	TLVH43x_C	0	70	°C
		TLVH43x_I	–40	85	
		TLVH43x_Q	–40	125	

(1) Maximum power dissipation is a function of $T_J(max)$, θ_{JA}, and T_A. The maximum allowable power dissipation at any allowable ambient temperature is $P_D = (T_J(max) – T_A) / \theta_{JA}$. Operating at the absolute maximum T_J of 150°C can affect reliability.

6.4 Thermal Information

THERMAL METRIC(1)		TLVH43xx					UNIT
		DCK (SC70)	PK (SOT-89)	DBV (SOT-23)	DBZ (SOT-23)	LP (TO-92)	
		6 PINS	3 PINS	5 PINS	3 PINS	3 PINS	
$R_{\theta JA}$	Junction-to-ambient thermal resistance	259	52	206	206	140	°C/W
$R_{\theta JC(top)}$	Junction-to-case (top) thermal resistance	87	9	131	76	55	°C/W

(1) For more information about traditional and new thermal metrics, see the *Semiconductor and IC Package Thermal Metrics* application report.

Copyright © 2023 Texas Instruments Incorporated

6.5 TLVH43x Electrical Characteristics

at 25°C free-air temperature (unless otherwise noted)

PARAMETER		TEST CONDITIONS			TLVH431 TLVH432			UNIT
					MIN	TYP	MAX	
V_{REF}	Reference voltage	$V_{KA} = V_{REF}$, I_K = 10 mA	T_A = 25°C		1.222	1.24	1.258	V
			T_A = full range, See 図 7-1 (1)	TLVH431C	1.21		1.27	
				TLVH431I	1.202		1.278	
				TLVH431Q	1.194		1.286	
$V_{REF(dev)}$	V_{REF} deviation over full temperature range(2)	$V_{KA} = V_{REF}$, I_K = 10 mA, See 図 7-1 (1)		TLVH431C		4	12	mV
				TLVH431I		6	20	
				TLVH431Q		11	31	
$\frac{\Delta V_{REF}}{\Delta V_{KA}}$	Ratio of V_{REF} change to cathode voltage change	I_K = 10 mA, $V_K = V_{REF}$ to 18 V, See 図 7-2				–1.5	–2.7	mV/V
I_{ref}	Reference terminal current	I_K = 10 mA, R1 = 10 kΩ, R2 = open, See 図 7-2				0.1	0.5	µA
$I_{ref(dev)}$	I_{ref} deviation over full temperature range(2)	I_K = 10 mA, R1 = 10 kΩ, R2 = open, See 図 7-2 (1)		TLVH431C		0.05	0.3	µA
				TLVH431I		0.1	0.4	
				TLVH431Q		0.15	0.5	
$I_{K(min)}$	Minimum cathode current for regulation	$V_{KA} = V_{REF}$, See 図 7-1				60	100	µA
$I_{K(off)}$	Off-state cathode current	V_{REF} = 0, V_{KA} = 18 V, See 図 7-3				0.02	0.1	µA
$\|z_{KA}\|$	Dynamic impedance(3)	$V_{KA} = V_{REF}$, f ≤ 1 kHz, I_K = 0.1 mA to 70 mA, See 図 7-1				0.25	0.4	Ω

(1) Full temperature ranges are –40°C to +125°C for TLVH431Q, –40°C to +85°C for TLVH431I, and 0°C to 70°C for TLVH431C.

(2) The deviation parameters $V_{REF(dev)}$ and $I_{ref(dev)}$ are defined as the differences between the maximum and minimum values obtained over the rated temperature range. The average full-range temperature coefficient of the reference input voltage, αV_{REF}, is defined as:

$$\left|\alpha V_{REF}\right|\left(\frac{ppm}{°C}\right) = \frac{\left(\frac{V_{REF(dev)}}{V_{REF}\left(T_A = 25°C\right)}\right) \times 10^6}{\Delta T_A}$$

where ΔT_A is the rated operating free-air temperature range of the device.
αV_{REF} can be positive or negative, depending on whether minimum V_{REF} or maximum V_{REF}, respectively, occurs at the lower temperature.

(3) The dynamic impedance is defined as:

$$\left|z_{ka}\right| = \frac{\Delta V_{KA}}{\Delta I_K}$$

When the device is operating with two external resistors (see 図 7-2), the total dynamic impedance of the circuit is defined as:

$$\left|z_{ka}\right|' = \frac{\Delta V}{\Delta I} \approx \left|z_{ka}\right| \times \left(1 + \frac{R1}{R2}\right)$$

Copyright © 2023 Texas Instruments Incorporated

6.6 TLVH43xA Electrical Characteristics

at 25°C free-air temperature (unless otherwise noted)

PARAMETER		TEST CONDITIONS			TLVH431A TLVH432A			UNIT
					MIN	TYP	MAX	
V_{REF}	Reference voltage	$V_{KA} = V_{REF}$, I_K = 10 mA	T_A = 25°C		1.228	1.24	1.252	V
			T_A = full range, See 図 7-1 (1)	TLVH431AC	1.221		1.259	
				TLVH431AI	1.215		1.265	
				TLVH431AQ	1.209		1.271	
$V_{REF(dev)}$	V_{REF} deviation over full temperature range(2)	$V_{KA} = V_{REF}$, I_K = 10 mA, See 図 7-1 (1)		TLVH431AC		4	12	mV
				TLVH431AI		6	20	
				TLVH431AQ		11	31	
$\frac{\Delta V_{REF}}{\Delta V_{KA}}$	Ratio of V_{REF} change to cathode voltage change	$V_K = V_{REF}$ to 18 V, I_K = 10 mA, See 図 7-2				–1.5	–2.7	mV/V
I_{ref}	Reference terminal current	I_K = 10 mA, R1 = 10 kΩ, R2 = open, See 図 7-2				0.1	0.5	µA
$I_{ref(dev)}$	I_{ref} deviation over full temperature range(2)	I_K = 10 mA, R1 = 10 kΩ, R2 = open, See 図 7-2 (1)		TLVH431AC		0.05	0.3	µA
				TLVH431AI		0.1	0.4	
				TLVH431AQ		0.15	0.5	
$I_{K(min)}$	Minimum cathode current for regulation	$V_{KA} = V_{REF}$, See 図 7-1				60	100	µA
$I_{K(off)}$	Off-state cathode current	V_{REF} = 0, V_{KA} = 18 V, See 図 7-3				0.02	0.1	µA
$\|z_{KA}\|$	Dynamic impedance(3)	$V_{KA} = V_{REF}$, f ≤ 1 kHz, I_K = 0.1 mA to 70 mA, See 図 7-1				0.25	0.4	Ω

(1) Full temperature ranges are –40°C to +125°C for TLVH431Q, –40°C to +85°C for TLVH431I, and 0°C to 70°C for TLVH431C.

(2) The deviation parameters $V_{REF(dev)}$ and $I_{ref(dev)}$ are defined as the differences between the maximum and minimum values obtained over the rated temperature range. The average full-range temperature coefficient of the reference input voltage, αV_{REF}, is defined as:

$$\left|\alpha V_{REF}\right|\left(\frac{ppm}{°C}\right) = \frac{\left(\frac{V_{REF(dev)}}{V_{REF}\left(T_A = 25°C\right)}\right) \times 10^6}{\Delta T_A}$$

where ΔT_A is the rated operating free-air temperature range of the device.
αV_{REF} can be positive or negative, depending on whether minimum V_{REF} or maximum V_{REF}, respectively, occurs at the lower temperature.

(3) The dynamic impedance is defined as:

$$\left|z_{ka}\right| = \frac{\Delta V_{KA}}{\Delta I_K}$$

When the device is operating with two external resistors (see 図 7-2), the total dynamic impedance of the circuit is defined as:

$$\left|z_{ka}\right|' = \frac{\Delta V}{\Delta I} \approx \left|z_{ka}\right| \times \left(1 + \frac{R1}{R2}\right)$$

Copyright © 2023 Texas Instruments Incorporated

6.7 TLVH43xB Electrical Characteristics

at 25°C free-air temperature (unless otherwise noted)

PARAMETER		TEST CONDITIONS			TLVH431B TLVH432B			UNIT
					MIN	TYP	MAX	
V_{REF}	Reference voltage	$V_{KA} = V_{REF}$, I_K = 10 mA	T_A = 25°C		1.234	1.24	1.246	V
			T_A = full range, See 図 7-1 (1)	TLVH431BC	1.227		1.253	
				TLVH431BI	1.224		1.259	
				TLVH431BQ	1.221		1.265	
$V_{REF(dev)}$	V_{REF} deviation over full temperature range(2)	$V_{KA} = V_{REF}$, I_K = 10 mA, See 図 7-1 (1)		TLVH431BC		4	12	mV
				TLVH431BI		6	20	
				TLVH431BQ		11	31	
$\frac{\Delta V_{REF}}{\Delta V_{KA}}$	Ratio of V_{REF} change to cathode voltage change	I_K = 10 mA, $V_K = V_{REF}$ to 18 V, See 図 7-2				–1.5	–2.7	mV/V
I_{ref}	Reference terminal current	I_K = 10 mA, R1 = 10 kΩ, R2 = open, See 図 7-2				0.1	0.5	µA
$I_{ref(dev)}$	I_{ref} deviation over full temperature range(2)	I_K = 10 mA, R1 = 10 kΩ, R2 = open, See 図 7-2 (1)		TLVH431BC		0.05	0.3	µA
				TLVH431BI		0.1	0.4	
				TLVH431BQ		0.15	0.5	
$I_{K(min)}$	Minimum cathode current for regulation	$V_{KA} = V_{REF}$, See 図 7-1				60	100	µA
$I_{K(off)}$	Off-state cathode current	V_{REF} = 0, V_{KA} = 18 V, See 図 7-3				0.02	0.1	µA
$\|z_{KA}\|$	Dynamic impedance(3)	$V_{KA} = V_{REF}$, f ≤ 1 kHz, I_K = 0.1 mA to 70 mA, See 図 7-1				0.25	0.4	Ω

(1) Full temperature ranges are –40°C to +125°C for TLVH431Q, –40°C to +85°C for TLVH431I, and 0°C to 70°C for TLVH431C.

(2) The deviation parameters $V_{REF(dev)}$ and $I_{ref(dev)}$ are defined as the differences between the maximum and minimum values obtained over the rated temperature range. The average full-range temperature coefficient of the reference input voltage, αV_{REF}, is defined as:

$$\left|\alpha V_{REF}\right|\left(\frac{ppm}{°C}\right) = \frac{\left(\frac{V_{REF(dev)}}{V_{REF}\left(T_A = 25°C\right)}\right) \times 10^6}{\Delta T_A}$$

where ΔT_A is the rated operating free-air temperature range of the device.
αV_{REF} can be positive or negative, depending on whether minimum V_{REF} or maximum V_{REF}, respectively, occurs at the lower temperature.

(3) The dynamic impedance is defined as:

$$\left|z_{ka}\right| = \frac{\Delta V_{KA}}{\Delta I_K}$$

When the device is operating with two external resistors (see 図 7-2), the total dynamic impedance of the circuit is defined as:

$$\left|z_{ka}\right|' = \frac{\Delta V}{\Delta I} \approx \left|z_{ka}\right| \times \left(1 + \frac{R1}{R2}\right)$$

6.8 Typical Characteristics

Operation of the device at these or any other conditions beyond those indicated in the *セクション 6.3* table are not implied.

Copyright © 2023 Texas Instruments Incorporated

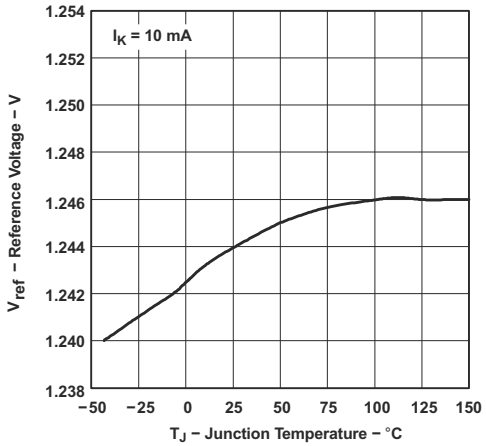


図 6-1. Reference Voltage vs Junction Temperature

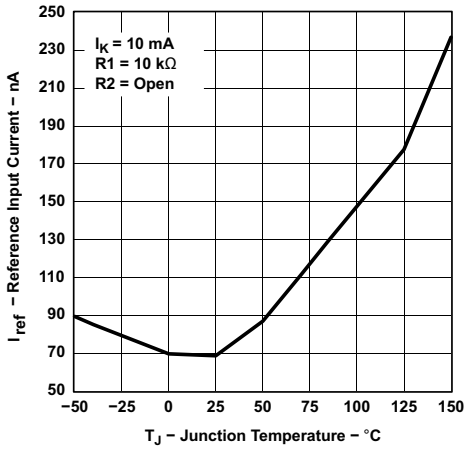


図 6-2. Reference Input Current vs Junction Temperature

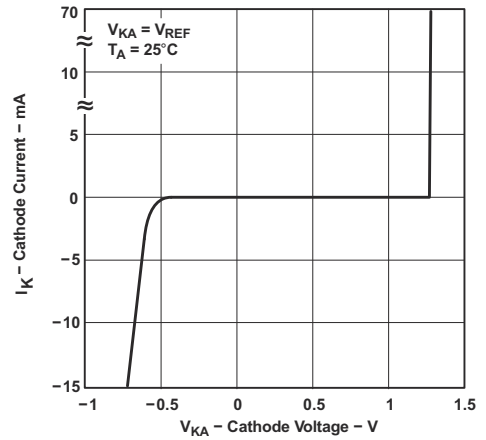


図 6-3. Cathode Current vs Cathode Voltage

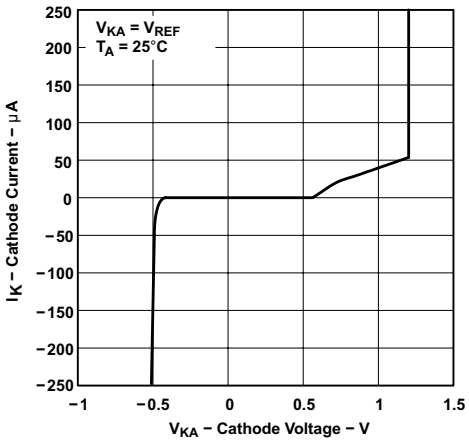


図 6-4. Cathode Current vs Cathode Voltage

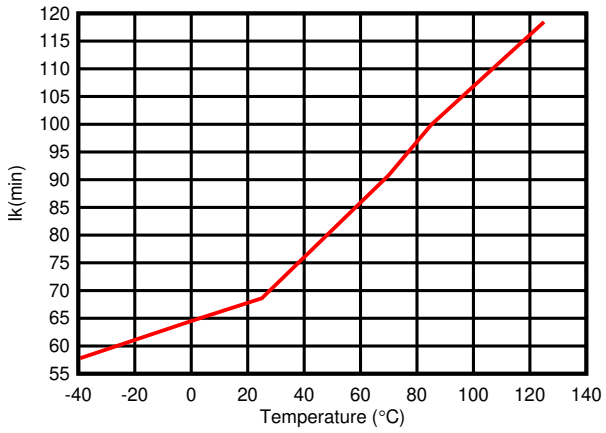


図 6-5. Minimum Cathode Current vs. Temperature

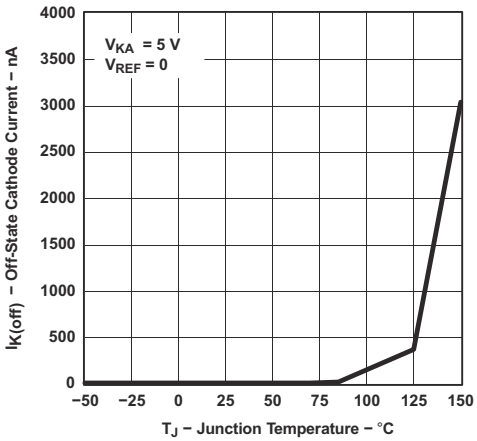


図 6-6. Off-State Cathode Current vs Junction Temperature

Copyright © 2023 Texas Instruments Incorporated

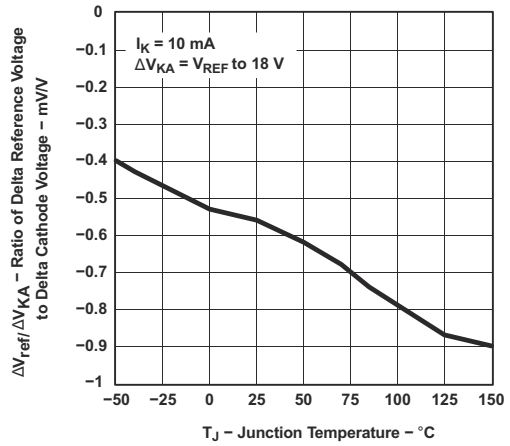
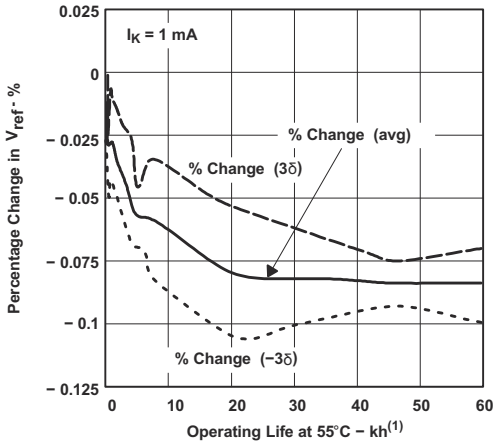


図 6-7. Ratio of Delta Reference Voltage to Delta Cathode Voltage vs Junction Temperature



(1) Extrapolated from life-test data taken at 125°C; the activation energy assumed is 0.7 eV.

図 6-8. Percentage Change in V_{REF} vs Operating Life at 55°C

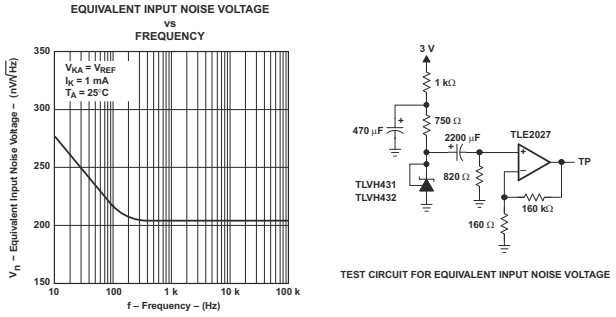


図 6-9. Equivalent Input Noise Voltage

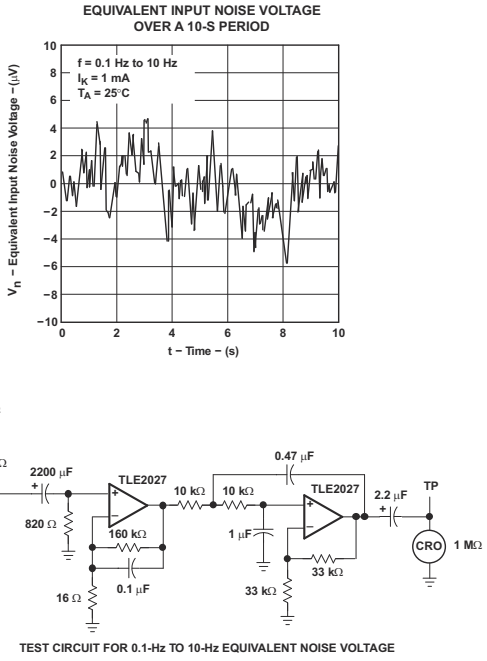


図 6-10. Equivalent Input Noise Voltage

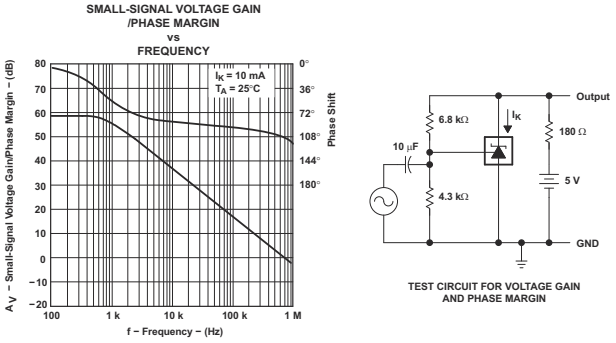


図 6-11. Voltage Gain and Phase Margin

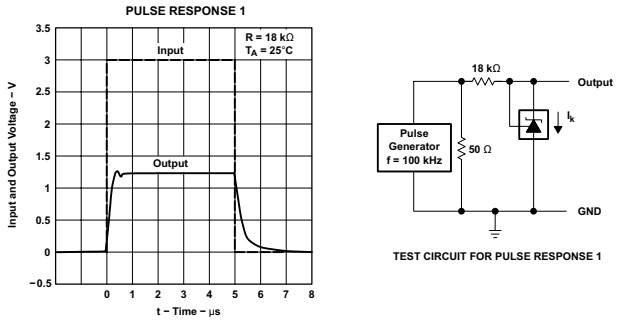


図 6-12. Pulse Response 1

Copyright © 2023 Texas Instruments Incorporated

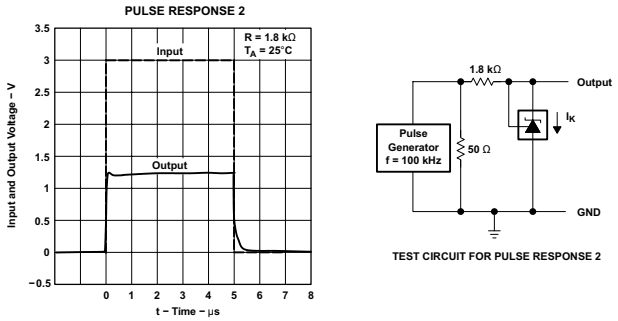


図 6-13. Pulse Response 2

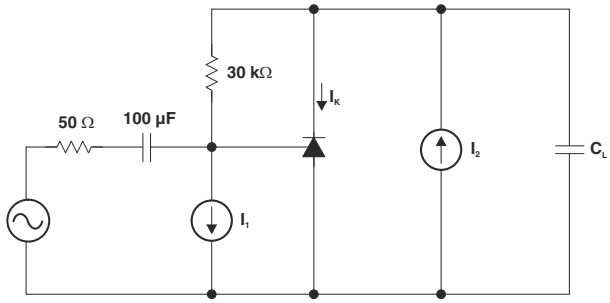


図 6-14. Phase Margin Test Circuit

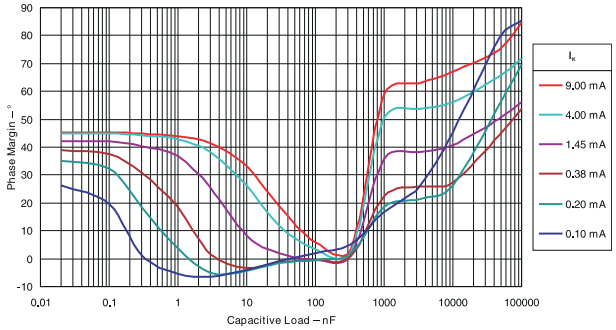


図 6-15. Phase Margin vs Capacitive Load V_{KA} = V_{REF} (1.25 V), T_A= 25°C

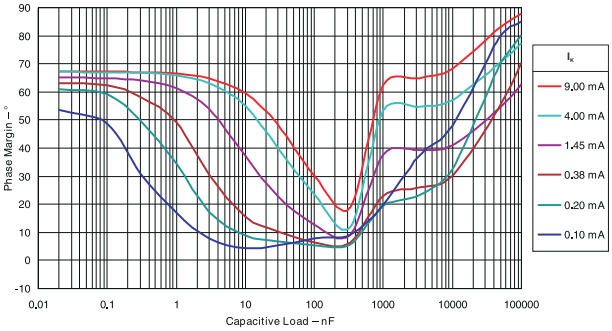


図 6-16. Phase Margin vs Capacitive Load V_{KA} = 2.50 V, T_A= 25°C

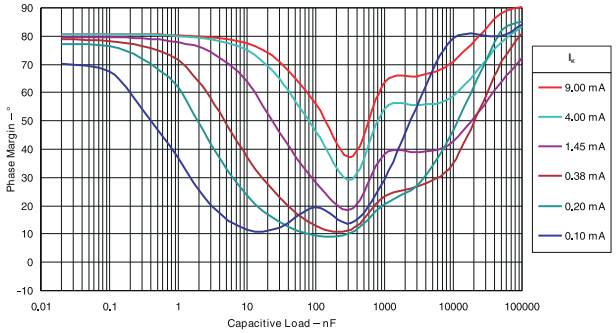


図 6-17. Phase Margin vs Capacitive Load V_{KA} = 5.00 V, T_A= 25°C

7 Parameter Measurement Information

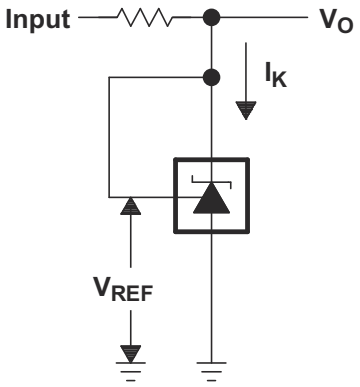


図 7-1. Test Circuit for $V_{KA} = V_{REF}$, $V_O = V_{KA} = V_{REF}$

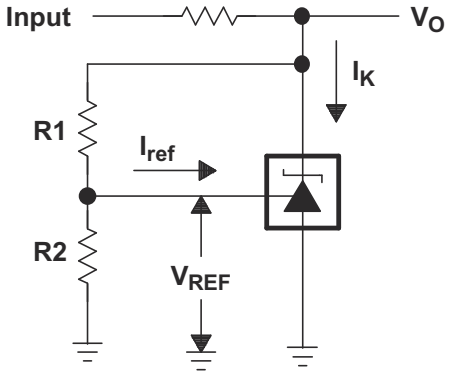


図 7-2. Test Circuit for $V_{KA} > V_{REF}$, $V_O = V_{KA} = V_{REF} \times (1 + R1/R2) + I_{ref} \times R1$

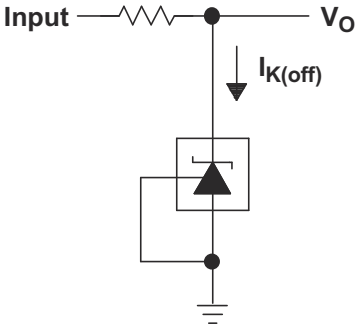


図 7-3. Test Circuit for $I_{K(off)}$

Copyright © 2023 Texas Instruments Incorporated

8 Detailed Description

8.1 Overview

TLVH431 is a low power counterpart to TL431, having lower reference voltage (1.24 V versus 2.5 V) for lower voltage adjustability and lower minimum cathode current ($I_{k(min)}$= 100 µA versus 1 mA). Like TL431, TLVH431 is used in conjunction with its key components to behave as a single voltage reference, error amplifier, voltage clamp or comparator with integrated reference.

TLVH431 is also a higher voltage counterpart to TLV431, with cathode voltage adjustability from 1.24 V to 18 V, making this part optimum for a wide range of end equipments in industrial, auto, telecom and computing. In order for this device to behave as a shunt regulator or error amplifier, >100 µA (I_{min}(max)) must be supplied in to the cathode pin. Under this condition, feedback can be applied from the Cathode and Ref pins to create a replica of the internal reference voltage.

Various reference voltage options can be purchased with initial tolerances (at 25°C) of 0.5%, 1%, and 1.5%. These reference options are denoted by B (0.5%), A (1.0%) and blank (1.5%) after the TLVH431.

The TLVH431xC devices are characterized for operation from 0°C to 70°C, the TLVH431xI devices are characterized for operation from –40°C to +85°C, and the TLVH431xQ devices are characterized for operation from –40°C to +125°C.

8.2 Functional Block Diagram

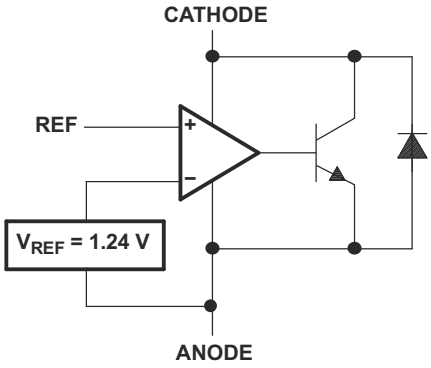


図 8-1. Equivalent Schematic

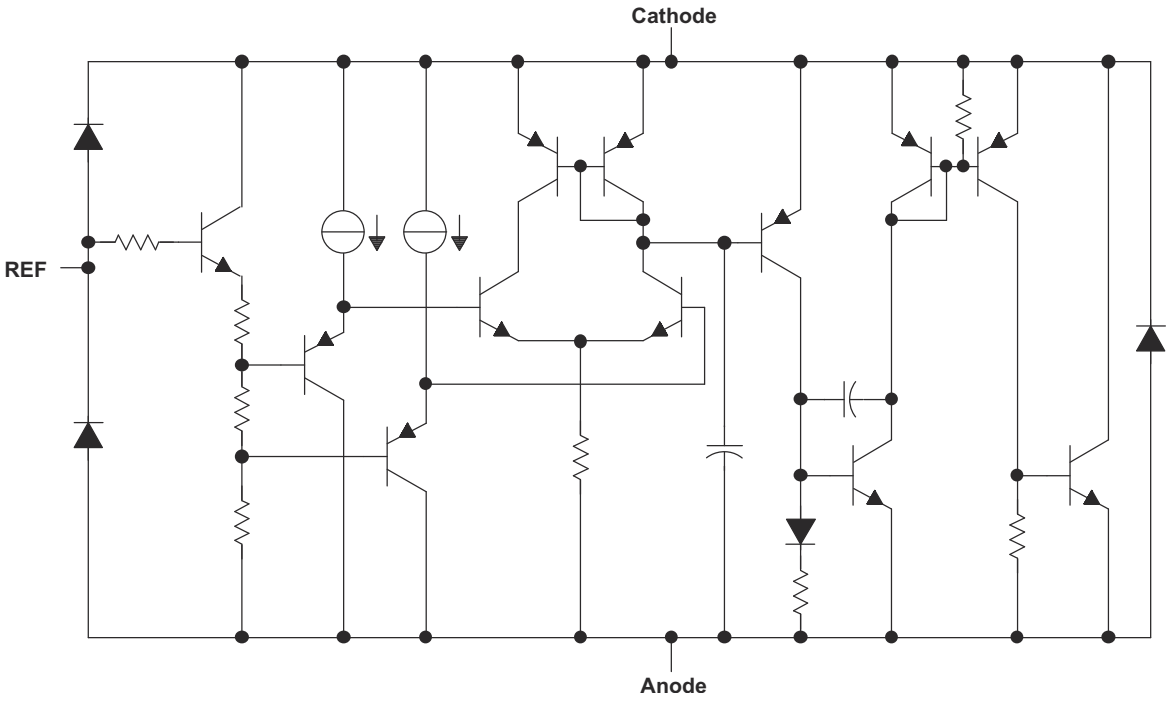


図 8-2. Detailed Schematic

8.3 Feature Description

TLVH431 consists of an internal reference and amplifier that outputs a sink current base on the difference between the reference pin and the virtual internal pin. The sink current is produced by an internal Darlington pair.

When operated with enough voltage headroom (≥ 1.24 V) and cathode current (I_{ka}), TLVH431 forces the reference pin to 1.24 V. However, the reference pin can not be left floating, as it needs Iref ≥ 0.5 µA (see *セクション 6*). This is because the reference pin is driven into an NPN, which needs base current in order operate properly.

When feedback is applied from the Cathode and Reference pins, TLVH431 behaves as a Zener diode, regulating to a constant voltage dependent on current being supplied into the cathode. This is due to the internal amplifier and reference entering the proper operating regions. The same amount of current needed in the above feedback situation must be applied to this device in open loop, servo or error amplifying implementations in order for it to be in the proper linear region giving TLVH431 enough gain.

Unlike many linear regulators, TLVH431 is internally compensated to be stable without an output capacitor between the cathode and anode. However, if it is desired to use an output capacitor 図 6-15, 図 6-16, and 図 6-17 can be used as a guide to assist in choosing the correct capacitor to maintain stability.

Copyright © 2023 Texas Instruments Incorporated

8.4 Device Functional Modes

8.4.1 Open Loop (Comparator)

When the cathode/output voltage or current of TLVH431 is not being fed back to the reference/input pin in any form, this device is operating in open loop. With proper cathode current (I_{ka}) applied to this device, TLVH431 has the characteristics shown in 図 6-4. With such high gain in this configuration, the TLVH431 device is typically used as a comparator. With the reference integrated makes TLVH431 the preferred choice when users are trying to monitor a certain level of a single signal.

8.4.2 Closed Loop

When the cathode/output voltage or current of TLVH431 is being fed back to the reference/input pin in any form, this device is operating in closed loop. The majority of applications involving TLVH431 use it in this manner to regulate a fixed voltage or current. The feedback enables this device to behave as an error amplifier, computing a portion of the output voltage and adjusting it to maintain the desired regulation. This is done by relating the output voltage back to the reference pin in a manner to make it equal to the internal reference voltage, which can be accomplished through resistive or direct feedback.

Copyright © 2023 Texas Instruments Incorporated

9 Applications and Implementation

注

以下のアプリケーション情報は、TI の製品仕様に含まれるものではなく、TI ではその正確性または完全性を保証いたしません。個々の目的に対する製品の適合性については、お客様の責任で判断していただくことになります。お客様は自身の設計実装を検証しテストすることで、システムの機能を確認する必要があります。

9.1 Application Information

図 9-1 shows the TLVH431, TLVH431A, or TLVH431B used in a 3.3-V isolated flyback supply. Output voltage V_O can be as low as reference voltage V_{REF} (1.24 V ± 1%). The output of the regulator, plus the forward voltage drop of the optocoupler LED (1.24 + 1.4 = 2.64 V), determine the minimum voltage that can be regulated in an isolated supply configuration. Regulated voltage as low as 2.7 Vdc is possible in the topology shown in 図 9-1.

The TLVH431 family of devices are prevalent in these applications, being designers go to choice for secondary side regulation. Due to this prevalence, this section explains operation and design in both states of TLVH431 that this application will see, open loop (Comparator + V_{REF}) and closed loop (Shunt Regulator).

Further information about system stability and using a TLVH431 device for compensation see *Compensation Design With TL431 for UCC28600*, SLUA671.

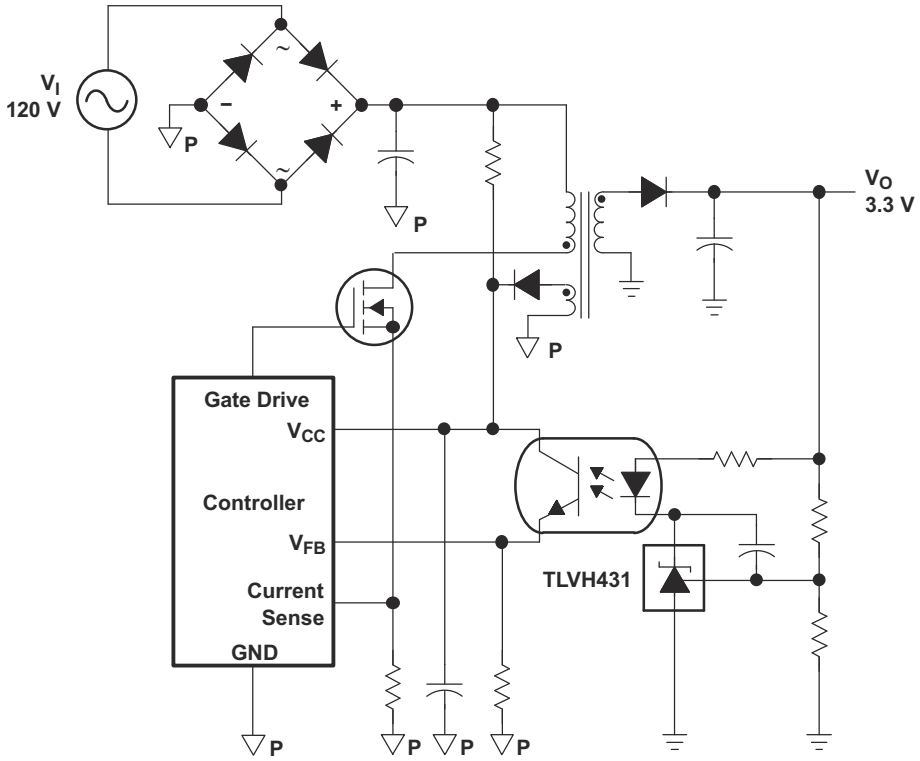


図 9-1. Flyback With Isolation Using TLVH431 or TLVH432 as Voltage Reference and Error Amplifier

Copyright © 2023 Texas Instruments Incorporated

9.2 Typical Applications

9.2.1 Comparator With Integrated Reference (Open Loop)

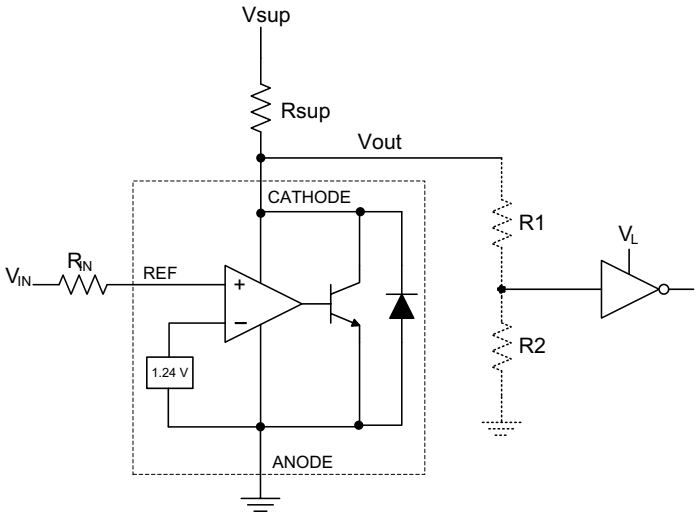


図 9-2. Comparator Application Schematic

9.2.1.1 Design Requirements

For this design example, use the parameters listed in 表 9-1 as the input parameters.

表 9-1. Design Parameters

DESIGN PARAMETER	EXAMPLE VALUE
Input Voltage Range	0 V to 5 V
Input Resistance	10 kΩ
Supply Voltage	9 V
Cathode Current (I_k)	500 µA
Output Voltage Level	~1 V - V_{sup}
Logic Input Thresholds V_{IH}/V_{IL}	V_L

9.2.1.2 Detailed Design Procedure

When using TLVH431 as a comparator with reference, determine the following:

- Input voltage range
- Reference voltage accuracy
- Output logic input high and low level thresholds
- Current source resistance

9.2.1.2.1 Basic Operation

In the configuration shown in 図 9-2, TLVH431 behaves as a comparator, comparing the V_{ref} pin voltage to the internal virtual reference voltage. When provided a proper cathode current (I_k), TLVH431 will have enough open loop gain to provide a quick response. With the TLVH431's max Operating Current (I_{min}) being 100 uA and up to 150 uA over temperature, operation below that could result in low gain, leading to a slow response.

Copyright © 2023 Texas Instruments Incorporated

9.2.1.2.2 Overdrive

Slow or inaccurate responses can also occur when the reference pin is not provided enough overdrive voltage. This is the amount of voltage that is higher than the internal virtual reference. The internal virtual reference voltage will be within the range of 1.24 V ±(0.5%, 1.0% or 1.5%) depending on which version is being used.

The more overdrive voltage provided, the faster the TLVH431 will respond. See figures 図 9-3 and 図 9-4, for the output responses to various input voltages.

For applications where TLVH431 is being used as a comparator, it is best to set the trip point to greater than the positive expected error (that is, +1.0% for the A version). For fast response, setting the trip point to > 10% of the internal V_{ref} should suffice.

For minimal voltage drop or difference from Vin to the ref pin, it is recommended to use an input resistor <10 kΩ to provide I_{ref}.

9.2.1.2.3 Output Voltage and Logic Input Level

In order for TLVH431 to properly be used as a comparator, the logic output must be readable by the receiving logic device. This is accomplished by knowing the input high and low level threshold voltage levels, typically denoted by V_{IH} and V_{IL}.

As shown in 図 9-3 and 図 9-4, TLVH431's output low level voltage in open-loop/comparator mode is approximately 1 V, which is sufficient for some 3.3 V supplied logic. However, would not work for 2.5 V and 1.8 V supplied logic. To accommodate this a resistive divider can be tied to the output to attenuate the output voltage to a voltage legible to the receiving low voltage logic device.

TLVH431's output high voltage is approximately V_{SUP} due to TLVH431 being open-collector. If V_{SUP} is much higher than the receiving logic's maximum input voltage tolerance, the output must be attenuated to accommodate the outgoing logic's reliability.

When using a resistive divider on the output, be sure to make the sum of the resistive divider (R1 and R2 in 図 9-2) is much greater than R_{SUP} in order to not interfere with TLVH431's ability to pull close to V_{SUP} when turning off.

9.2.1.2.3.1 Input Resistance

TLVH431 requires an input resistance in this application in order to source the reference current (I_{REF}) needed from this device to be in the proper operating regions while turning on. The actual voltage seen at the ref pin will be $V_{REF} = V_{IN} – I_{REF} × R_{IN}$. Because I_{REF} can be as high as 0.5 µA, TI recommends to use a resistance small enough that will mitigate the error that I_{REF} creates from V_{IN}.

9.2.1.3 Application Curves

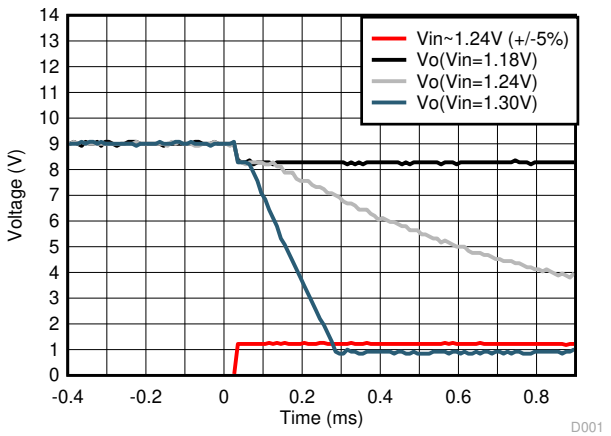


図 9-3. Output Response With Small Overdrive Voltages

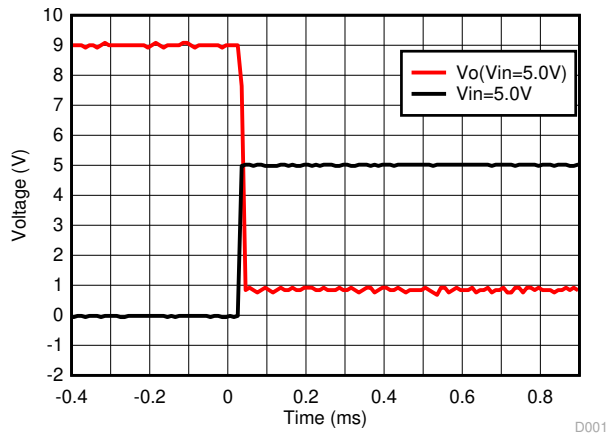
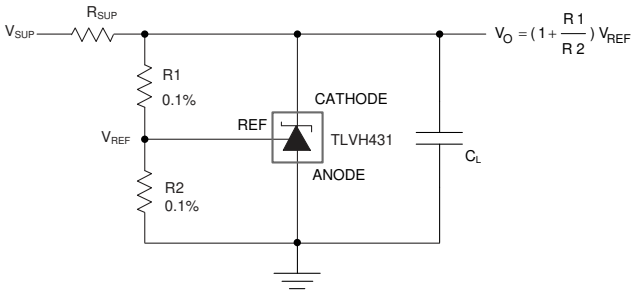


図 9-4. Output Response With Large Overdrive Voltage

Copyright © 2023 Texas Instruments Incorporated

9.2.2 Shunt Regulator/Reference



Copyright © 2016, Texas Instruments Incorporated

図 9-5. Shunt Regulator Schematic

9.2.2.1 Design Requirements

For this design example, use the parameters listed in 表 9-2 as the input parameters.

表 9-2. Design Parameters

DESIGN PARAMETER	EXAMPLE VALUE
Reference Initial Accuracy	1.0%
Supply Voltage	6 V
Cathode Current (Ik)	500 µA
Output Voltage Level	1.24 V - 18 V
Load Capacitance	4.7 µF
Feedback Resistor Values and Accuracy (R1 and R2)	10 kΩ

9.2.2.2 Detailed Design Procedure

When using TLVH431 as a Shunt Regulator, determine the following:

- Input voltage range
- Temperature range
- Total accuracy
- Cathode current
- Reference initial accuracy
- Output capacitance

9.2.2.2.1 Programming Output/Cathode Voltage

To program the cathode voltage to a regulated voltage a resistive bridge must be shunted between the cathode and anode pins with the mid point tied to the reference pin. This can be seen in 図 9-5, with R1 and R2 being the resistive bridge. The cathode/output voltage in the shunt regulator configuration can be approximated by the equation shown in 図 9-5. The cathode voltage can be more accurately determined by taking in to account the cathode current:

$V_O=(1+R1/R2) \times V_{REF}-I_{REF} \times R1$

In order for this equation to be valid, TLVH431 must be fully biased so that it has enough open loop gain to mitigate any gain error. This can be done by meeting the I_{min} spec denoted in *セクション 6*.

9.2.2.2.2 Total Accuracy

When programming the output above unity gain (V_{KA}=V_{REF}), TLVH431 is susceptible to other errors that may effect the overall accuracy beyond V_{REF}. These errors include:

- R1 and R2 accuracies
- $V_{I(dev)}$ - Change in reference voltage over temperature
- ΔV_{ref} / ΔV_{KA} - Change in reference voltage to the change in cathode voltage
- $|z_{KA}|$ - Dynamic impedance, causing a change in cathode voltage with cathode current

Worst case, cathode voltage can be determined taking all of the variables in to account. The application note *Setting the Shunt Voltage on an Adjustable Shunt Regulator,* SLVA445, assists designers in setting the shunt voltage to achieve optimum accuracy for this device.

9.2.2.2.3 Stability

Though TLVH431 is stable with no capacitive load, the device that receives the shunt regulator's output voltage could present a capacitive load that is within the TLVH431 region of stability, shown in 図 6-15, 図 6-16 and 図 6-17. Also, designers may use capacitive loads to improve the transient response or for power supply decoupling.

TI recommends to choose capacitors that will give a phase margin > 5° to guarantee stability of the TLVH431.

9.2.2.3 Application Curve

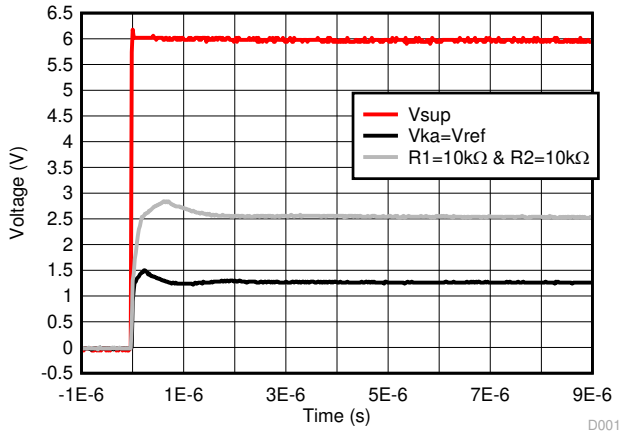


図 9-6. TLVH431 Start-up Response

Copyright © 2023 Texas Instruments Incorporated

10 Power Supply Recommendations

When using TLVH431 as a Linear Regulator to supply a load, designers will typically use a bypass capacitor on the output/cathode pin. When doing this, be sure that the capacitance is within the stability criteria shown in 図 6-15, 図 6-16, and 図 6-17.

To not exceed the maximum cathode current, be sure that the supply voltage is current limited. Also, limit the current being driven into the Ref pin, as not to exceed its absolute maximum rating.

For applications shunting high currents, pay attention to the cathode and anode trace lengths, adjusting the width of the traces to have the proper current density.

Copyright © 2023 Texas Instruments Incorporated

11 Layout

11.1 Layout Guidelines

Place decoupling capacitors as close to the device as possible. Use appropriate widths for traces when shunting high currents to avoid excessive voltage drops.

11.2 Layout Example

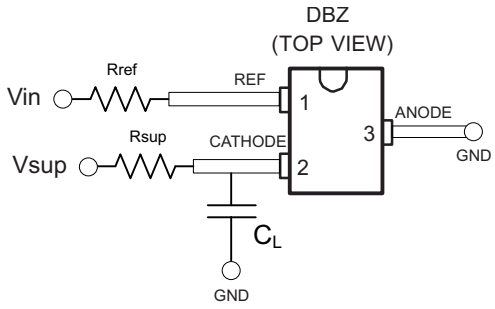


図 11-1. DBZ Layout example

Copyright © 2023 Texas Instruments Incorporated

12 Device and Documentation Support

12.1 Documentation Support

12.1.1 Related Documentation

For related documentation see the following:

-
- , *Compensation Design With TL431 for UCC28600*SLUA671
- *Setting the Shunt Voltage on an Adjustable Shunt Regulator,*SLVA445

12.2 ドキュメントの更新通知を受け取る方法

ドキュメントの更新についての通知を受け取るには、ti.com のデバイス製品フォルダを開いてください。「更新の通知を受け取る」をクリックして登録すると、変更されたすべての製品情報に関するダイジェストを毎週受け取れます。変更の詳細については、修正されたドキュメントに含まれている改訂履歴をご覧ください。

12.3 サポート・リソース

TI E2E™ サポート・フォーラムは、エンジニアが検証済みの回答と設計に関するヒントをエキスパートから迅速かつ直接得ることができる場所です。既存の回答を検索したり、独自の質問をしたりすることで、設計で必要な支援を迅速に得ることができます。

リンクされているコンテンツは、該当する貢献者により、現状のまま提供されるものです。これらは TI の仕様を構成するものではなく、必ずしも TI の見解を反映したものではありません。TI の使用条件を参照してください。

12.4 Related Links

The table below lists quick access links. Categories include technical documents, support and community resources, tools and software, and quick access to sample or buy.

表 12-1. Related Links

PARTS	PRODUCT FOLDER	SAMPLE & BUY	TECHNICAL DOCUMENTS	TOOLS & SOFTWARE	SUPPORT & COMMUNITY
TLVH431	Click here	Click here	Click here	Click here	Click here
TLVH431A	Click here	Click here	Click here	Click here	Click here
TLVH431B	Click here	Click here	Click here	Click here	Click here
TLVH432	Click here	Click here	Click here	Click here	Click here
TLVH432A	Click here	Click here	Click here	Click here	Click here
TLVH432B	Click here	Click here	Click here	Click here	Click here

12.5 Trademarks

TI E2E™ is a trademark of Texas Instruments.
すべての商標は、それぞれの所有者に帰属します。

12.6 静電気放電に関する注意事項

この IC は、ESD によって破損する可能性があります。テキサス・インスツルメンツは、IC を取り扱う際には常に適切な注意を払うことを推奨します。正しい取り扱いおよび設置手順に従わない場合、デバイスを破損するおそれがあります。

ESD による破損は、わずかな性能低下からデバイスの完全な故障まで多岐にわたります。精密な IC の場合、パラメータがわずかに変化するだけで公表されている仕様から外れる可能性があるため、破損が発生しやすくなっています。

12.7 用語集

テキサス・インスツルメンツ用語集　　この用語集には、用語や略語の一覧および定義が記載されています。

13 Mechanical, Packaging, and Orderable Information

The following pages include mechanical packaging and orderable information. This information is the most current data available for the designated devices. This data is subject to change without notice and revision of this document. For browser based versions of this data sheet, refer to the left hand navigation.

Copyright © 2023 Texas Instruments Incorporated

PACKAGING INFORMATION

Orderable Device	Status (1)	Package Type	Package Drawing	Pins	Package Qty	Eco Plan (2)	Lead finish/ Ball material (6)	MSL Peak Temp (3)	Op Temp (°C)	Device Marking (4/5)	Samples
TLVH431ACDBVR	ACTIVE	SOT-23	DBV	5	3000	RoHS & Green	NIPDAU | SN	Level-1-260C-UNLIM	0 to 70	(Y3PG, Y3PJ, Y3PU)	Samples
TLVH431ACDBVT	ACTIVE	SOT-23	DBV	5	250	RoHS & Green	NIPDAU | SN	Level-1-260C-UNLIM	0 to 70	(Y3PG, Y3PJ, Y3PU)	Samples
TLVH431ACDBZR	ACTIVE	SOT-23	DBZ	3	3000	RoHS & Green	NIPDAU | SN | NIPDAUAG	Level-1-260C-UNLIM	0 to 70	(Y3PS, Y3PU)	Samples
TLVH431ACDBZRG4	ACTIVE	SOT-23	DBZ	3	3000	RoHS & Green	NIPDAUAG	Level-1-260C-UNLIM	0 to 70	Y3PS	Samples
TLVH431ACDBZT	ACTIVE	SOT-23	DBZ	3	250	RoHS & Green	NIPDAU | SN | NIPDAUAG	Level-1-260C-UNLIM	0 to 70	(Y3PS, Y3PU)	Samples
TLVH431ACDCKR	ACTIVE	SC70	DCK	6	3000	RoHS & Green	NIPDAU	Level-1-260C-UNLIM	0 to 70	YPU	Samples
TLVH431ACDCKT	ACTIVE	SC70	DCK	6	250	RoHS & Green	NIPDAU	Level-1-260C-UNLIM	0 to 70	YPU	Samples
TLVH431ACLPR	ACTIVE	TO-92	LP	3	2000	RoHS & Green	SN	N / A for Pkg Type	0 to 70	ZA431A	Samples
TLVH431ACPK	ACTIVE	SOT-89	PK	3	1000	RoHS & Green	SN	Level-2-260C-1 YEAR	0 to 70	W2	Samples
TLVH431AIDBVR	ACTIVE	SOT-23	DBV	5	3000	RoHS & Green	NIPDAU | SN	Level-1-260C-UNLIM	-40 to 85	(Y3TJ, Y3TU)	Samples
TLVH431AIDBVT	ACTIVE	SOT-23	DBV	5	250	RoHS & Green	NIPDAU | SN	Level-1-260C-UNLIM	-40 to 85	(Y3TJ, Y3TU)	Samples
TLVH431AIDBZR	ACTIVE	SOT-23	DBZ	3	3000	RoHS & Green	NIPDAU | SN | NIPDAUAG	Level-1-260C-UNLIM	-40 to 85	(Y3T3, Y3TS, Y3TU)	Samples
TLVH431AIDBZT	ACTIVE	SOT-23	DBZ	3	250	RoHS & Green	NIPDAU | SN | NIPDAUAG	Level-1-260C-UNLIM	-40 to 85	(Y3TS, Y3TU)	Samples
TLVH431AIDBZTG4	ACTIVE	SOT-23	DBZ	3	250	RoHS & Green	NIPDAUAG	Level-1-260C-UNLIM	-40 to 85	Y3TS	Samples
TLVH431AIDCKR	ACTIVE	SC70	DCK	6	3000	RoHS & Green	NIPDAU	Level-1-260C-UNLIM	-40 to 85	YTU	Samples
TLVH431AIDCKT	ACTIVE	SC70	DCK	6	250	RoHS & Green	NIPDAU	Level-1-260C-UNLIM	-40 to 85	YTU	Samples
TLVH431AILPR	ACTIVE	TO-92	LP	3	2000	RoHS & Green	SN	N / A for Pkg Type	-40 to 85	ZB431A	Samples
TLVH431AIPK	ACTIVE	SOT-89	PK	3	1000	RoHS & Green	SN	Level-2-260C-1 YEAR	-40 to 85	W3	Samples
TLVH431AQDBVR	ACTIVE	SOT-23	DBV	5	3000	RoHS & Green	NIPDAU | SN	Level-1-260C-UNLIM	-40 to 125	(Y3NJ, Y3NU)	Samples

Orderable Device	Status (1)	Package Type	Package Drawing	Pins	Package Qty	Eco Plan (2)	Lead finish/ Ball material (6)	MSL Peak Temp (3)	Op Temp (°C)	Device Marking (4/5)	Samples
TLVH431AQDBVT	ACTIVE	SOT-23	DBV	5	250	RoHS & Green	NIPDAU | SN	Level-1-260C-UNLIM	-40 to 125	(Y3NJ, Y3NU)	Samples
TLVH431AQDBZR	ACTIVE	SOT-23	DBZ	3	3000	RoHS & Green	NIPDAU | SN | NIPDAUAG	Level-1-260C-UNLIM	-40 to 125	(Y3NS, Y3NU)	Samples
TLVH431AQDBZT	ACTIVE	SOT-23	DBZ	3	250	RoHS & Green	NIPDAU | SN | NIPDAUAG	Level-1-260C-UNLIM	-40 to 125	(Y3NS, Y3NU)	Samples
TLVH431AQDBZTG4	ACTIVE	SOT-23	DBZ	3	250	RoHS & Green	NIPDAUAG	Level-1-260C-UNLIM	-40 to 125	Y3NS	Samples
TLVH431AQDCKR	ACTIVE	SC70	DCK	6	3000	RoHS & Green	NIPDAU	Level-1-260C-UNLIM	-40 to 125	YNU	Samples
TLVH431AQDCKT	ACTIVE	SC70	DCK	6	250	RoHS & Green	NIPDAU	Level-1-260C-UNLIM	-40 to 125	YNU	Samples
TLVH431AQLPR	ACTIVE	TO-92	LP	3	2000	RoHS & Green	SN	N / A for Pkg Type	-40 to 125	ZD431A	Samples
TLVH431AQPK	ACTIVE	SOT-89	PK	3	1000	RoHS & Green	SN	Level-2-260C-1 YEAR	-40 to 125	VD	Samples
TLVH431BCDBVR	ACTIVE	SOT-23	DBV	5	3000	RoHS & Green	NIPDAU | SN	Level-1-260C-UNLIM	0 to 70	(Y3JJ, Y3JU)	Samples
TLVH431BCDBVT	ACTIVE	SOT-23	DBV	5	250	RoHS & Green	NIPDAU | SN	Level-1-260C-UNLIM	0 to 70	(Y3JJ, Y3JU)	Samples
TLVH431BCDBZR	ACTIVE	SOT-23	DBZ	3	3000	RoHS & Green	NIPDAU | SN | NIPDAUAG	Level-1-260C-UNLIM	0 to 70	(Y3J3, Y3JS, Y3JU)	Samples
TLVH431BCDBZT	ACTIVE	SOT-23	DBZ	3	250	RoHS & Green	NIPDAU | SN | NIPDAUAG	Level-1-260C-UNLIM	0 to 70	(Y3JS, Y3JU)	Samples
TLVH431BCDBZTG4	ACTIVE	SOT-23	DBZ	3	250	RoHS & Green	NIPDAUAG	Level-1-260C-UNLIM	0 to 70	Y3JS	Samples
TLVH431BCDCKR	ACTIVE	SC70	DCK	6	3000	RoHS & Green	NIPDAU	Level-1-260C-UNLIM	0 to 70	YHU	Samples
TLVH431BCDCKT	ACTIVE	SC70	DCK	6	250	RoHS & Green	NIPDAU	Level-1-260C-UNLIM	0 to 70	YHU	Samples
TLVH431BCLP	ACTIVE	TO-92	LP	3	1000	RoHS & Green	SN	N / A for Pkg Type	0 to 70	ZA431B	Samples
TLVH431BCLPR	ACTIVE	TO-92	LP	3	2000	RoHS & Green	SN	N / A for Pkg Type	0 to 70	ZA431B	Samples
TLVH431BCPK	ACTIVE	SOT-89	PK	3	1000	RoHS & Green	SN	Level-2-260C-1 YEAR	0 to 70	V7	Samples
TLVH431BIDBVR	ACTIVE	SOT-23	DBV	5	3000	RoHS & Green	NIPDAU | SN	Level-1-260C-UNLIM	-40 to 85	(Y3KJ, Y3KU)	Samples
TLVH431BIDBVT	ACTIVE	SOT-23	DBV	5	250	RoHS & Green	NIPDAU | SN	Level-1-260C-UNLIM	-40 to 85	(Y3KJ, Y3KU)	Samples

Orderable Device	Status (1)	Package Type	Package Drawing	Pins	Package Qty	Eco Plan (2)	Lead finish/ Ball material (6)	MSL Peak Temp (3)	Op Temp (°C)	Device Marking (4/5)	Samples
TLVH431BIDBZR	ACTIVE	SOT-23	DBZ	3	3000	RoHS & Green	NIPDAU | SN | NIPDAUAG	Level-1-260C-UNLIM	-40 to 85	(Y3K3, Y3KS, Y3KU)	Samples
TLVH431BIDBZRG4	ACTIVE	SOT-23	DBZ	3	3000	RoHS & Green	NIPDAUAG	Level-1-260C-UNLIM	-40 to 85	Y3KS	Samples
TLVH431BIDBZT	ACTIVE	SOT-23	DBZ	3	250	RoHS & Green	NIPDAU | SN | NIPDAUAG	Level-1-260C-UNLIM	-40 to 85	(Y3KS, Y3KU)	Samples
TLVH431BIDCKR	ACTIVE	SC70	DCK	6	3000	RoHS & Green	NIPDAU	Level-1-260C-UNLIM	-40 to 85	YJU	Samples
TLVH431BIDCKT	ACTIVE	SC70	DCK	6	250	RoHS & Green	NIPDAU	Level-1-260C-UNLIM	-40 to 85	YJU	Samples
TLVH431BILP	ACTIVE	TO-92	LP	3	1000	RoHS & Green	SN	N / A for Pkg Type	-40 to 85	ZB431B	Samples
TLVH431BILPR	ACTIVE	TO-92	LP	3	2000	RoHS & Green	SN	N / A for Pkg Type	-40 to 85	ZB431B	Samples
TLVH431BIPK	ACTIVE	SOT-89	PK	3	1000	RoHS & Green	SN	Level-2-260C-1 YEAR	-40 to 85	V8	Samples
TLVH431BQDBVR	ACTIVE	SOT-23	DBV	5	3000	RoHS & Green	NIPDAU | SN	Level-1-260C-UNLIM	-40 to 125	(Y3LJ, Y3LU)	Samples
TLVH431BQDBVT	ACTIVE	SOT-23	DBV	5	250	RoHS & Green	NIPDAU | SN	Level-1-260C-UNLIM	-40 to 125	(Y3LJ, Y3LU)	Samples
TLVH431BQDBZR	ACTIVE	SOT-23	DBZ	3	3000	RoHS & Green	NIPDAU | SN | NIPDAUAG	Level-1-260C-UNLIM	-40 to 125	(Y3LS, Y3LU)	Samples
TLVH431BQDBZRG4	ACTIVE	SOT-23	DBZ	3	3000	RoHS & Green	NIPDAUAG	Level-1-260C-UNLIM	-40 to 125	Y3LS	Samples
TLVH431BQDBZT	ACTIVE	SOT-23	DBZ	3	250	RoHS & Green	NIPDAU | SN | NIPDAUAG	Level-1-260C-UNLIM	-40 to 125	(Y3LS, Y3LU)	Samples
TLVH431BQDCKR	ACTIVE	SC70	DCK	6	3000	RoHS & Green	NIPDAU	Level-1-260C-UNLIM	-40 to 125	YKU	Samples
TLVH431BQDCKT	ACTIVE	SC70	DCK	6	250	RoHS & Green	NIPDAU	Level-1-260C-UNLIM	-40 to 125	YKU	Samples
TLVH431BQLPR	ACTIVE	TO-92	LP	3	2000	RoHS & Green	SN	N / A for Pkg Type	-40 to 125	ZD431B	Samples
TLVH431BQPK	ACTIVE	SOT-89	PK	3	1000	RoHS & Green	SN	Level-2-260C-1 YEAR	-40 to 125	V9	Samples
TLVH431CDBVR	ACTIVE	SOT-23	DBV	5	3000	RoHS & Green	NIPDAU | SN	Level-1-260C-UNLIM	0 to 70	(Y3UJ, Y3UU)	Samples
TLVH431CDBVT	ACTIVE	SOT-23	DBV	5	250	RoHS & Green	NIPDAU | SN	Level-1-260C-UNLIM	0 to 70	(Y3UJ, Y3UU)	Samples
TLVH431CDBZR	ACTIVE	SOT-23	DBZ	3	3000	RoHS & Green	NIPDAU | SN | NIPDAUAG	Level-1-260C-UNLIM	0 to 70	(Y3US, Y3UU)	Samples

Orderable Device	Status (1)	Package Type	Package Drawing	Pins	Package Qty	Eco Plan (2)	Lead finish/ Ball material (6)	MSL Peak Temp (3)	Op Temp (°C)	Device Marking (4/5)	Samples
TLVH431CDBZT	ACTIVE	SOT-23	DBZ	3	250	RoHS & Green	NIPDAU | SN | NIPDAUAG	Level-1-260C-UNLIM	0 to 70	(Y3US, Y3UU)	Samples
TLVH431CDBZTG4	ACTIVE	SOT-23	DBZ	3	250	RoHS & Green	NIPDAUAG	Level-1-260C-UNLIM	0 to 70	Y3US	Samples
TLVH431CDCKT	ACTIVE	SC70	DCK	6	250	RoHS & Green	NIPDAU	Level-1-260C-UNLIM	0 to 70	YUU	Samples
TLVH431CLPR	ACTIVE	TO-92	LP	3	2000	RoHS & Green	SN	N / A for Pkg Type	0 to 70	ZA431	Samples
TLVH431CPK	ACTIVE	SOT-89	PK	3	1000	RoHS & Green	SN	Level-2-260C-1 YEAR	0 to 70	W4	Samples
TLVH431IDBVR	ACTIVE	SOT-23	DBV	5	3000	RoHS & Green	NIPDAU | SN	Level-1-260C-UNLIM	-40 to 85	(Y3VJ, Y3VU)	Samples
TLVH431IDBVT	ACTIVE	SOT-23	DBV	5	250	RoHS & Green	NIPDAU | SN	Level-1-260C-UNLIM	-40 to 85	(Y3VJ, Y3VU)	Samples
TLVH431IDBZR	ACTIVE	SOT-23	DBZ	3	3000	RoHS & Green	NIPDAU | SN | NIPDAUAG	Level-1-260C-UNLIM	-40 to 85	(Y3VS, Y3VU)	Samples
TLVH431IDBZT	ACTIVE	SOT-23	DBZ	3	250	RoHS & Green	NIPDAU | SN | NIPDAUAG	Level-1-260C-UNLIM	-40 to 85	(Y3VS, Y3VU)	Samples
TLVH431IDCKR	ACTIVE	SC70	DCK	6	3000	RoHS & Green	NIPDAU	Level-1-260C-UNLIM	-40 to 85	YVU	Samples
TLVH431IDCKT	ACTIVE	SC70	DCK	6	250	RoHS & Green	NIPDAU	Level-1-260C-UNLIM	-40 to 85	YVU	Samples
TLVH431ILPR	ACTIVE	TO-92	LP	3	2000	RoHS & Green	SN	N / A for Pkg Type	-40 to 85	ZB431	Samples
TLVH431IPK	ACTIVE	SOT-89	PK	3	1000	RoHS & Green	SN	Level-2-260C-1 YEAR	-40 to 85	W5	Samples
TLVH431QDBVR	ACTIVE	SOT-23	DBV	5	3000	RoHS & Green	NIPDAU | SN	Level-1-260C-UNLIM	-40 to 125	(Y3MJ, Y3MU)	Samples
TLVH431QDBVT	ACTIVE	SOT-23	DBV	5	250	RoHS & Green	NIPDAU | SN	Level-1-260C-UNLIM	-40 to 125	(Y3MJ, Y3MU)	Samples
TLVH431QDBZR	ACTIVE	SOT-23	DBZ	3	3000	RoHS & Green	NIPDAU | SN | NIPDAUAG	Level-1-260C-UNLIM	-40 to 125	(Y3MS, Y3MU)	Samples
TLVH431QDBZT	ACTIVE	SOT-23	DBZ	3	250	RoHS & Green	NIPDAU | SN | NIPDAUAG	Level-1-260C-UNLIM	-40 to 125	(Y3MS, Y3MU)	Samples
TLVH431QDBZTG4	ACTIVE	SOT-23	DBZ	3	250	RoHS & Green	NIPDAUAG	Level-1-260C-UNLIM	-40 to 125	Y3MS	Samples
TLVH431QDCKR	ACTIVE	SC70	DCK	6	3000	RoHS & Green	NIPDAU	Level-1-260C-UNLIM	-40 to 125	YMU	Samples
TLVH431QDCKT	ACTIVE	SC70	DCK	6	250	RoHS & Green	NIPDAU	Level-1-260C-UNLIM	-40 to 125	YMU	Samples

Orderable Device	Status (1)	Package Type	Package Drawing	Pins	Package Qty	Eco Plan (2)	Lead finish/ Ball material (6)	MSL Peak Temp (3)	Op Temp (°C)	Device Marking (4/5)	Samples
TLVH431QLPR	ACTIVE	TO-92	LP	3	2000	RoHS & Green	SN	N / A for Pkg Type	-40 to 125	ZD431	Samples
TLVH431QPK	ACTIVE	SOT-89	PK	3	1000	RoHS & Green	SN	Level-2-260C-1 YEAR	-40 to 125	VC	Samples
TLVH432ACDBZR	ACTIVE	SOT-23	DBZ	3	3000	RoHS & Green	NIPDAU | SN | NIPDAUAG	Level-1-260C-UNLIM	0 to 70	(Y2ES, Y2EU)	Samples
TLVH432ACDBZRG4	ACTIVE	SOT-23	DBZ	3	3000	RoHS & Green	NIPDAUAG	Level-1-260C-UNLIM	0 to 70	Y2ES	Samples
TLVH432ACDBZT	ACTIVE	SOT-23	DBZ	3	250	RoHS & Green	NIPDAU | SN | NIPDAUAG	Level-1-260C-UNLIM	0 to 70	(Y2ES, Y2EU)	Samples
TLVH432AIDBZR	ACTIVE	SOT-23	DBZ	3	3000	RoHS & Green	NIPDAU | SN | NIPDAUAG	Level-1-260C-UNLIM	-40 to 85	(Y2FS, Y2FU)	Samples
TLVH432AIPK	ACTIVE	SOT-89	PK	3	1000	RoHS & Green	SN	Level-2-260C-1 YEAR	-40 to 85	VL	Samples
TLVH432AQDBZR	ACTIVE	SOT-23	DBZ	3	3000	RoHS & Green	NIPDAU | SN | NIPDAUAG	Level-1-260C-UNLIM	-40 to 125	(Y2GS, Y2GU)	Samples
TLVH432AQDBZT	ACTIVE	SOT-23	DBZ	3	250	RoHS & Green	NIPDAU | SN | NIPDAUAG	Level-1-260C-UNLIM	-40 to 125	(Y2GS, Y2GU)	Samples
TLVH432BCDBZR	ACTIVE	SOT-23	DBZ	3	3000	RoHS & Green	NIPDAU | SN | NIPDAUAG	Level-1-260C-UNLIM	0 to 70	(Y2HS, Y2HU)	Samples
TLVH432BCDBZRG4	ACTIVE	SOT-23	DBZ	3	3000	RoHS & Green	NIPDAUAG	Level-1-260C-UNLIM	0 to 70	Y2HS	Samples
TLVH432BCPK	ACTIVE	SOT-89	PK	3	1000	RoHS & Green	SN	Level-2-260C-1 YEAR	0 to 70	VN	Samples
TLVH432BIDBZR	ACTIVE	SOT-23	DBZ	3	3000	RoHS & Green	NIPDAU | SN | NIPDAUAG	Level-1-260C-UNLIM	-40 to 85	(Y2JS, Y2JU)	Samples
TLVH432BQDBZR	ACTIVE	SOT-23	DBZ	3	3000	RoHS & Green	NIPDAU | SN | NIPDAUAG	Level-1-260C-UNLIM	-40 to 125	(Y2KS, Y2KU)	Samples
TLVH432BQDBZT	ACTIVE	SOT-23	DBZ	3	250	RoHS & Green	NIPDAU | SN | NIPDAUAG	Level-1-260C-UNLIM	-40 to 125	(Y2KS, Y2KU)	Samples
TLVH432BQDBZTG4	ACTIVE	SOT-23	DBZ	3	250	RoHS & Green	NIPDAUAG	Level-1-260C-UNLIM	-40 to 125	Y2KS	Samples
TLVH432CDBZR	ACTIVE	SOT-23	DBZ	3	3000	RoHS & Green	NIPDAU | SN | NIPDAUAG	Level-1-260C-UNLIM	0 to 70	(Y2AS, Y2AU)	Samples
TLVH432CDBZT	ACTIVE	SOT-23	DBZ	3	250	RoHS & Green	NIPDAU | SN | NIPDAUAG	Level-1-260C-UNLIM	0 to 70	(Y2AS, Y2AU)	Samples
TLVH432CPK	ACTIVE	SOT-89	PK	3	1000	RoHS & Green	SN	Level-2-260C-1 YEAR	0 to 70	VG	Samples

Orderable Device	Status (1)	Package Type	Package Drawing	Pins	Package Qty	Eco Plan (2)	Lead finish/ Ball material (6)	MSL Peak Temp (3)	Op Temp (°C)	Device Marking (4/5)	Samples
TLVH432IDBZR	ACTIVE	SOT-23	DBZ	3	3000	RoHS & Green	NIPDAU | SN | NIPDAUAG	Level-1-260C-UNLIM	-40 to 85	(Y2BS, Y2BU)	Samples
TLVH432QDBZR	ACTIVE	SOT-23	DBZ	3	3000	RoHS & Green	NIPDAU | SN | NIPDAUAG	Level-1-260C-UNLIM	-40 to 125	(Y2DS, Y2DU)	Samples
TLVH432QDBZT	ACTIVE	SOT-23	DBZ	3	250	RoHS & Green	NIPDAU | SN | NIPDAUAG	Level-1-260C-UNLIM	-40 to 125	(Y2DS, Y2DU)	Samples
TLVH432QPK	ACTIVE	SOT-89	PK	3	1000	RoHS & Green	SN	Level-2-260C-1 YEAR	-40 to 125	VJ	Samples

(1) The marketing status values are defined as follows:
ACTIVE: Product device recommended for new designs.
LIFEBUY: TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.
NRND: Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.
PREVIEW: Device has been announced but is not in production. Samples may or may not be available.
OBSOLETE: TI has discontinued the production of the device.

(2) **RoHS:** TI defines "RoHS" to mean semiconductor products that are compliant with the current EU RoHS requirements for all 10 RoHS substances, including the requirement that RoHS substance do not exceed 0.1% by weight in homogeneous materials. Where designed to be soldered at high temperatures, "RoHS" products are suitable for use in specified lead-free processes. TI may reference these types of products as "Pb-Free".
RoHS Exempt: TI defines "RoHS Exempt" to mean products that contain lead but are compliant with EU RoHS pursuant to a specific EU RoHS exemption.
Green: TI defines "Green" to mean the content of Chlorine (Cl) and Bromine (Br) based flame retardants meet JS709B low halogen requirements of <=1000ppm threshold. Antimony trioxide based flame retardants must also meet the <=1000ppm threshold requirement.

(3) MSL, Peak Temp. - The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.

(4) There may be additional marking, which relates to the logo, the lot trace code information, or the environmental category on the device.

(5) Multiple Device Markings will be inside parentheses. Only one Device Marking contained in parentheses and separated by a "~" will appear on a device. If a line is indented then it is a continuation of the previous line and the two combined represent the entire Device Marking for that device.

(6) Lead finish/Ball material - Orderable Devices may have multiple material finish options. Finish options are separated by a vertical ruled line. Lead finish/Ball material values may wrap to two lines if the finish value exceeds the maximum column width.

Important Information and Disclaimer: The information provided on this page represents TI's knowledge and belief as of the date that it is provided. TI bases its knowledge and belief on information provided by third parties, and makes no representation or warranty as to the accuracy of such information. Efforts are underway to better integrate information from third parties. TI has taken and continues to take reasonable steps to provide representative and accurate information but may not have conducted destructive testing or chemical analysis on incoming materials and chemicals. TI and TI suppliers consider certain information to be proprietary, and thus CAS numbers and other limited information may not be available for release.

In no event shall TI's liability arising out of such information exceed the total purchase price of the TI part(s) at issue in this document sold by TI to Customer on an annual basis.

OTHER QUALIFIED VERSIONS OF TLVH431A, TLVH431B :

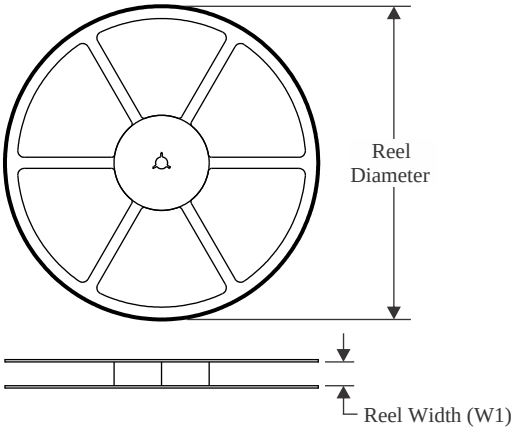
- Automotive : TLVH431A-Q1, TLVH431B-Q1
- Enhanced Product : TLVH431B-EP

NOTE: Qualified Version Definitions:

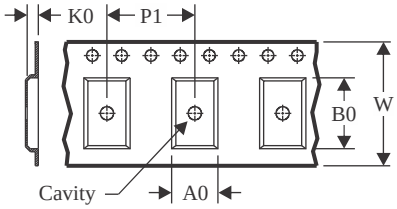
- Automotive - Q100 devices qualified for high-reliability automotive applications targeting zero defects
- Enhanced Product - Supports Defense, Aerospace and Medical Applications

TAPE AND REEL INFORMATION

REEL DIMENSIONS

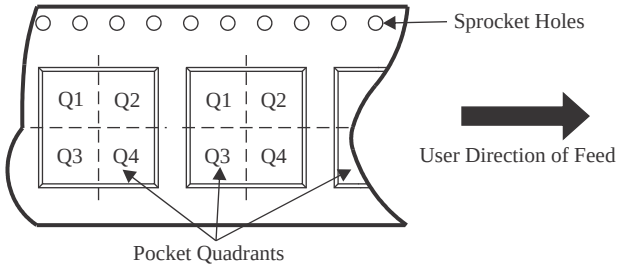


TAPE DIMENSIONS



A0	Dimension designed to accommodate the component width
B0	Dimension designed to accommodate the component length
K0	Dimension designed to accommodate the component thickness
W	Overall width of the carrier tape
P1	Pitch between successive cavity centers

QUADRANT ASSIGNMENTS FOR PIN 1 ORIENTATION IN TAPE



*All dimensions are nominal

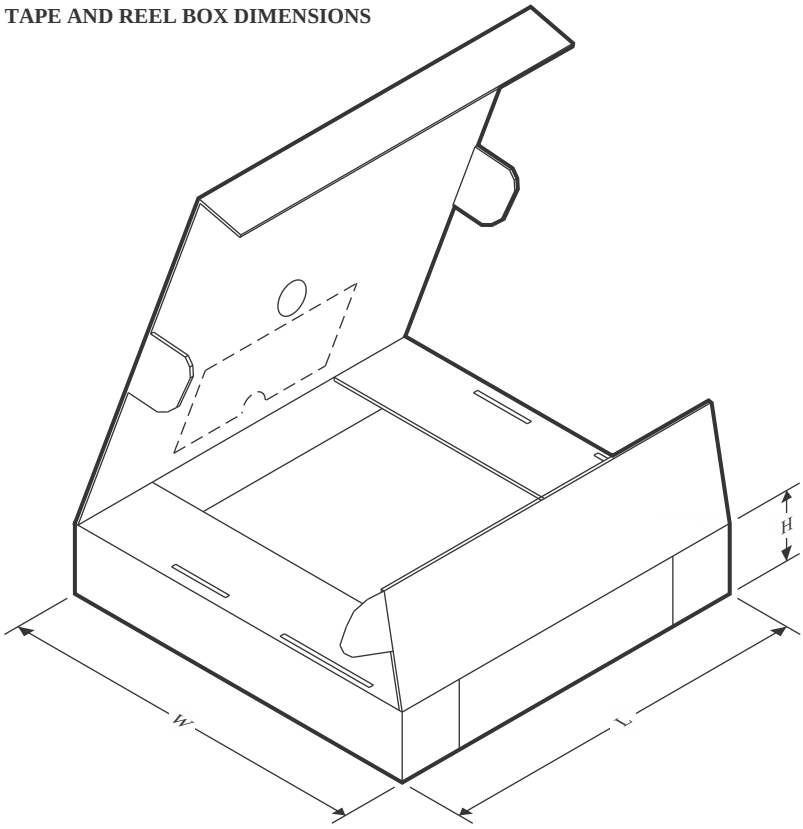
Device	Package Type	Package Drawing	Pins	SPQ	Reel Diameter (mm)	Reel Width W1 (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P1 (mm)	W (mm)	Pin1 Quadrant
TLVH431ACDBVR	SOT-23	DBV	5	3000	178.0	9.0	3.3	3.2	1.4	4.0	8.0	Q3
TLVH431ACDBVT	SOT-23	DBV	5	250	178.0	9.0	3.3	3.2	1.4	4.0	8.0	Q3
TLVH431ACDBZR	SOT-23	DBZ	3	3000	178.0	9.0	3.15	2.77	1.22	4.0	8.0	Q3
TLVH431ACDBZRG4	SOT-23	DBZ	3	3000	180.0	8.4	3.15	2.77	1.22	4.0	8.0	Q3
TLVH431ACDBZT	SOT-23	DBZ	3	250	178.0	9.0	3.15	2.77	1.22	4.0	8.0	Q3
TLVH431ACDCKR	SC70	DCK	6	3000	179.0	8.4	2.2	2.5	1.2	4.0	8.0	Q3
TLVH431ACDCKT	SC70	DCK	6	250	179.0	8.4	2.2	2.5	1.2	4.0	8.0	Q3
TLVH431ACPK	SOT-89	PK	3	1000	180.0	12.4	4.91	4.52	1.9	8.0	12.0	Q3
TLVH431AIDBVR	SOT-23	DBV	5	3000	178.0	9.0	3.3	3.2	1.4	4.0	8.0	Q3
TLVH431AIDBVR	SOT-23	DBV	5	3000	180.0	8.4	3.2	3.2	1.4	4.0	8.0	Q3
TLVH431AIDBVT	SOT-23	DBV	5	250	180.0	8.4	3.2	3.2	1.4	4.0	8.0	Q3
TLVH431AIDBVT	SOT-23	DBV	5	250	178.0	9.0	3.3	3.2	1.4	4.0	8.0	Q3
TLVH431AIDBZR	SOT-23	DBZ	3	3000	179.0	8.4	3.15	2.95	1.22	4.0	8.0	Q3
TLVH431AIDBZR	SOT-23	DBZ	3	3000	180.0	8.4	3.15	2.77	1.22	4.0	8.0	Q3
TLVH431AIDBZR	SOT-23	DBZ	3	3000	178.0	9.0	3.15	2.77	1.22	4.0	8.0	Q3
TLVH431AIDBZR	SOT-23	DBZ	3	3000	178.0	9.2	3.15	2.77	1.22	4.0	8.0	Q3

Device	Package Type	Package Drawing	Pins	SPQ	Reel Diameter (mm)	Reel Width W1 (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P1 (mm)	W (mm)	Pin1 Quadrant
TLVH431AIDBZT	SOT-23	DBZ	3	250	178.0	9.0	3.15	2.77	1.22	4.0	8.0	Q3
TLVH431AIDBZTG4	SOT-23	DBZ	3	250	180.0	8.4	3.15	2.77	1.22	4.0	8.0	Q3
TLVH431AIDCKR	SC70	DCK	6	3000	179.0	8.4	2.2	2.5	1.2	4.0	8.0	Q3
TLVH431AIDCKT	SC70	DCK	6	250	179.0	8.4	2.2	2.5	1.2	4.0	8.0	Q3
TLVH431AIPK	SOT-89	PK	3	1000	180.0	12.4	4.91	4.52	1.9	8.0	12.0	Q3
TLVH431AQDBVR	SOT-23	DBV	5	3000	180.0	8.4	3.2	3.2	1.4	4.0	8.0	Q3
TLVH431AQDBVR	SOT-23	DBV	5	3000	178.0	9.0	3.3	3.2	1.4	4.0	8.0	Q3
TLVH431AQDBVT	SOT-23	DBV	5	250	178.0	9.0	3.3	3.2	1.4	4.0	8.0	Q3
TLVH431AQDBVT	SOT-23	DBV	5	250	180.0	8.4	3.2	3.2	1.4	4.0	8.0	Q3
TLVH431AQDBZR	SOT-23	DBZ	3	3000	178.0	9.0	3.15	2.77	1.22	4.0	8.0	Q3
TLVH431AQDBZR	SOT-23	DBZ	3	3000	180.0	8.4	3.15	2.77	1.22	4.0	8.0	Q3
TLVH431AQDBZT	SOT-23	DBZ	3	250	178.0	9.0	3.15	2.77	1.22	4.0	8.0	Q3
TLVH431AQDBZTG4	SOT-23	DBZ	3	250	180.0	8.4	3.15	2.77	1.22	4.0	8.0	Q3
TLVH431AQDCKR	SC70	DCK	6	3000	179.0	8.4	2.2	2.5	1.2	4.0	8.0	Q3
TLVH431AQDCKT	SC70	DCK	6	250	179.0	8.4	2.2	2.5	1.2	4.0	8.0	Q3
TLVH431AQPK	SOT-89	PK	3	1000	180.0	12.4	4.91	4.52	1.9	8.0	12.0	Q3
TLVH431BCDBVR	SOT-23	DBV	5	3000	178.0	9.0	3.3	3.2	1.4	4.0	8.0	Q3
TLVH431BCDBVT	SOT-23	DBV	5	250	178.0	9.0	3.3	3.2	1.4	4.0	8.0	Q3
TLVH431BCDBVT	SOT-23	DBV	5	250	180.0	8.4	3.2	3.2	1.4	4.0	8.0	Q3
TLVH431BCDBZR	SOT-23	DBZ	3	3000	178.0	9.0	3.15	2.77	1.22	4.0	8.0	Q3
TLVH431BCDBZT	SOT-23	DBZ	3	250	178.0	9.0	3.15	2.77	1.22	4.0	8.0	Q3
TLVH431BCDBZTG4	SOT-23	DBZ	3	250	180.0	8.4	3.15	2.77	1.22	4.0	8.0	Q3
TLVH431BCDCKR	SC70	DCK	6	3000	179.0	8.4	2.2	2.5	1.2	4.0	8.0	Q3
TLVH431BCDCKT	SC70	DCK	6	250	179.0	8.4	2.2	2.5	1.2	4.0	8.0	Q3
TLVH431BCPK	SOT-89	PK	3	1000	180.0	12.4	4.91	4.52	1.9	8.0	12.0	Q3
TLVH431BIDBVR	SOT-23	DBV	5	3000	180.0	8.4	3.2	3.2	1.4	4.0	8.0	Q3
TLVH431BIDBVR	SOT-23	DBV	5	3000	178.0	9.0	3.3	3.2	1.4	4.0	8.0	Q3
TLVH431BIDBVT	SOT-23	DBV	5	250	180.0	8.4	3.2	3.2	1.4	4.0	8.0	Q3
TLVH431BIDBVT	SOT-23	DBV	5	250	178.0	9.0	3.3	3.2	1.4	4.0	8.0	Q3
TLVH431BIDBZR	SOT-23	DBZ	3	3000	178.0	9.2	3.15	2.77	1.22	4.0	8.0	Q3
TLVH431BIDBZR	SOT-23	DBZ	3	3000	178.0	9.0	3.15	2.77	1.22	4.0	8.0	Q3
TLVH431BIDBZR	SOT-23	DBZ	3	3000	180.0	8.4	3.15	2.77	1.22	4.0	8.0	Q3
TLVH431BIDBZR	SOT-23	DBZ	3	3000	179.0	8.4	3.15	2.95	1.22	4.0	8.0	Q3
TLVH431BIDBZRG4	SOT-23	DBZ	3	3000	180.0	8.4	3.15	2.77	1.22	4.0	8.0	Q3
TLVH431BIDBZT	SOT-23	DBZ	3	250	178.0	9.0	3.15	2.77	1.22	4.0	8.0	Q3
TLVH431BIDCKR	SC70	DCK	6	3000	179.0	8.4	2.2	2.5	1.2	4.0	8.0	Q3
TLVH431BIDCKT	SC70	DCK	6	250	179.0	8.4	2.2	2.5	1.2	4.0	8.0	Q3
TLVH431BIPK	SOT-89	PK	3	1000	180.0	12.4	4.91	4.52	1.9	8.0	12.0	Q3
TLVH431BQDBVR	SOT-23	DBV	5	3000	178.0	9.0	3.3	3.2	1.4	4.0	8.0	Q3
TLVH431BQDBVR	SOT-23	DBV	5	3000	179.0	8.4	3.2	3.2	1.4	4.0	8.0	Q3
TLVH431BQDBVT	SOT-23	DBV	5	250	179.0	8.4	3.2	3.2	1.4	4.0	8.0	Q3

Device	Package Type	Package Drawing	Pins	SPQ	Reel Diameter (mm)	Reel Width W1 (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P1 (mm)	W (mm)	Pin1 Quadrant
TLVH431BQDBVT	SOT-23	DBV	5	250	178.0	9.0	3.3	3.2	1.4	4.0	8.0	Q3
TLVH431BQDBZR	SOT-23	DBZ	3	3000	180.0	8.4	3.15	2.77	1.22	4.0	8.0	Q3
TLVH431BQDBZR	SOT-23	DBZ	3	3000	178.0	9.0	3.15	2.77	1.22	4.0	8.0	Q3
TLVH431BQDBZRG4	SOT-23	DBZ	3	3000	180.0	8.4	3.15	2.77	1.22	4.0	8.0	Q3
TLVH431BQDBZT	SOT-23	DBZ	3	250	178.0	9.0	3.15	2.77	1.22	4.0	8.0	Q3
TLVH431BQDCKR	SC70	DCK	6	3000	179.0	8.4	2.2	2.5	1.2	4.0	8.0	Q3
TLVH431BQDCKT	SC70	DCK	6	250	179.0	8.4	2.2	2.5	1.2	4.0	8.0	Q3
TLVH431BQPK	SOT-89	PK	3	1000	180.0	12.4	4.91	4.52	1.9	8.0	12.0	Q3
TLVH431CDBVR	SOT-23	DBV	5	3000	178.0	9.0	3.3	3.2	1.4	4.0	8.0	Q3
TLVH431CDBVR	SOT-23	DBV	5	3000	180.0	8.4	3.2	3.2	1.4	4.0	8.0	Q3
TLVH431CDBVT	SOT-23	DBV	5	250	179.0	8.4	3.2	3.2	1.4	4.0	8.0	Q3
TLVH431CDBVT	SOT-23	DBV	5	250	178.0	9.0	3.3	3.2	1.4	4.0	8.0	Q3
TLVH431CDBZR	SOT-23	DBZ	3	3000	178.0	9.0	3.15	2.77	1.22	4.0	8.0	Q3
TLVH431CDBZT	SOT-23	DBZ	3	250	178.0	9.0	3.15	2.77	1.22	4.0	8.0	Q3
TLVH431CDBZTG4	SOT-23	DBZ	3	250	180.0	8.4	3.15	2.77	1.22	4.0	8.0	Q3
TLVH431CDCKT	SC70	DCK	6	250	179.0	8.4	2.2	2.5	1.2	4.0	8.0	Q3
TLVH431CPK	SOT-89	PK	3	1000	180.0	12.4	4.91	4.52	1.9	8.0	12.0	Q3
TLVH431IDBVR	SOT-23	DBV	5	3000	178.0	9.0	3.3	3.2	1.4	4.0	8.0	Q3
TLVH431IDBVR	SOT-23	DBV	5	3000	180.0	8.4	3.2	3.2	1.4	4.0	8.0	Q3
TLVH431IDBVT	SOT-23	DBV	5	250	180.0	8.4	3.2	3.2	1.4	4.0	8.0	Q3
TLVH431IDBVT	SOT-23	DBV	5	250	178.0	9.0	3.3	3.2	1.4	4.0	8.0	Q3
TLVH431IDBZR	SOT-23	DBZ	3	3000	178.0	9.0	3.15	2.77	1.22	4.0	8.0	Q3
TLVH431IDBZR	SOT-23	DBZ	3	3000	180.0	8.4	3.15	2.77	1.22	4.0	8.0	Q3
TLVH431IDBZT	SOT-23	DBZ	3	250	178.0	9.0	3.15	2.77	1.22	4.0	8.0	Q3
TLVH431IDCKR	SC70	DCK	6	3000	179.0	8.4	2.2	2.5	1.2	4.0	8.0	Q3
TLVH431IDCKT	SC70	DCK	6	250	179.0	8.4	2.2	2.5	1.2	4.0	8.0	Q3
TLVH431IPK	SOT-89	PK	3	1000	180.0	12.4	4.91	4.52	1.9	8.0	12.0	Q3
TLVH431QDBVR	SOT-23	DBV	5	3000	180.0	8.4	3.2	3.2	1.4	4.0	8.0	Q3
TLVH431QDBVR	SOT-23	DBV	5	3000	178.0	9.0	3.3	3.2	1.4	4.0	8.0	Q3
TLVH431QDBVT	SOT-23	DBV	5	250	178.0	9.0	3.3	3.2	1.4	4.0	8.0	Q3
TLVH431QDBVT	SOT-23	DBV	5	250	180.0	8.4	3.2	3.2	1.4	4.0	8.0	Q3
TLVH431QDBZR	SOT-23	DBZ	3	3000	178.0	9.0	3.15	2.77	1.22	4.0	8.0	Q3
TLVH431QDBZT	SOT-23	DBZ	3	250	178.0	9.0	3.15	2.77	1.22	4.0	8.0	Q3
TLVH431QDBZTG4	SOT-23	DBZ	3	250	180.0	8.4	3.15	2.77	1.22	4.0	8.0	Q3
TLVH431QDCKR	SC70	DCK	6	3000	179.0	8.4	2.2	2.5	1.2	4.0	8.0	Q3
TLVH431QDCKT	SC70	DCK	6	250	179.0	8.4	2.2	2.5	1.2	4.0	8.0	Q3
TLVH431QPK	SOT-89	PK	3	1000	180.0	12.4	4.91	4.52	1.9	8.0	12.0	Q3
TLVH432ACDBZR	SOT-23	DBZ	3	3000	178.0	9.0	3.15	2.77	1.22	4.0	8.0	Q3
TLVH432ACDBZRG4	SOT-23	DBZ	3	3000	180.0	8.4	3.15	2.77	1.22	4.0	8.0	Q3
TLVH432ACDBZT	SOT-23	DBZ	3	250	178.0	9.0	3.15	2.77	1.22	4.0	8.0	Q3
TLVH432AIDBZR	SOT-23	DBZ	3	3000	178.0	9.0	3.15	2.77	1.22	4.0	8.0	Q3

Device	Package Type	Package Drawing	Pins	SPQ	Reel Diameter (mm)	Reel Width W1 (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P1 (mm)	W (mm)	Pin1 Quadrant
TLVH432AIPK	SOT-89	PK	3	1000	180.0	12.4	4.91	4.52	1.9	8.0	12.0	Q3
TLVH432AQDBZR	SOT-23	DBZ	3	3000	178.0	9.0	3.15	2.77	1.22	4.0	8.0	Q3
TLVH432AQDBZT	SOT-23	DBZ	3	250	178.0	9.0	3.15	2.77	1.22	4.0	8.0	Q3
TLVH432BCDBZR	SOT-23	DBZ	3	3000	178.0	9.0	3.15	2.77	1.22	4.0	8.0	Q3
TLVH432BCDBZRG4	SOT-23	DBZ	3	3000	180.0	8.4	3.15	2.77	1.22	4.0	8.0	Q3
TLVH432BCPK	SOT-89	PK	3	1000	180.0	12.4	4.91	4.52	1.9	8.0	12.0	Q3
TLVH432BIDBZR	SOT-23	DBZ	3	3000	178.0	9.0	3.15	2.77	1.22	4.0	8.0	Q3
TLVH432BQDBZR	SOT-23	DBZ	3	3000	178.0	9.0	3.15	2.77	1.22	4.0	8.0	Q3
TLVH432BQDBZT	SOT-23	DBZ	3	250	178.0	9.0	3.15	2.77	1.22	4.0	8.0	Q3
TLVH432BQDBZTG4	SOT-23	DBZ	3	250	180.0	8.4	3.15	2.77	1.22	4.0	8.0	Q3
TLVH432CDBZR	SOT-23	DBZ	3	3000	178.0	9.0	3.15	2.77	1.22	4.0	8.0	Q3
TLVH432CDBZT	SOT-23	DBZ	3	250	178.0	9.0	3.15	2.77	1.22	4.0	8.0	Q3
TLVH432CPK	SOT-89	PK	3	1000	180.0	12.4	4.91	4.52	1.9	8.0	12.0	Q3
TLVH432IDBZR	SOT-23	DBZ	3	3000	178.0	9.0	3.15	2.77	1.22	4.0	8.0	Q3
TLVH432QDBZR	SOT-23	DBZ	3	3000	178.0	9.0	3.15	2.77	1.22	4.0	8.0	Q3
TLVH432QDBZT	SOT-23	DBZ	3	250	178.0	9.0	3.15	2.77	1.22	4.0	8.0	Q3
TLVH432QPK	SOT-89	PK	3	1000	180.0	12.4	4.91	4.52	1.9	8.0	12.0	Q3

TAPE AND REEL BOX DIMENSIONS



*All dimensions are nominal

Device	Package Type	Package Drawing	Pins	SPQ	Length (mm)	Width (mm)	Height (mm)
TLVH431ACDBVR	SOT-23	DBV	5	3000	180.0	180.0	18.0
TLVH431ACDBVT	SOT-23	DBV	5	250	180.0	180.0	18.0
TLVH431ACDBZR	SOT-23	DBZ	3	3000	180.0	180.0	18.0
TLVH431ACDBZRG4	SOT-23	DBZ	3	3000	183.0	183.0	20.0
TLVH431ACDBZT	SOT-23	DBZ	3	250	180.0	180.0	18.0
TLVH431ACDCKR	SC70	DCK	6	3000	200.0	183.0	25.0
TLVH431ACDCKT	SC70	DCK	6	250	203.0	203.0	35.0
TLVH431ACPK	SOT-89	PK	3	1000	340.0	340.0	38.0
TLVH431AIDBVR	SOT-23	DBV	5	3000	180.0	180.0	18.0
TLVH431AIDBVR	SOT-23	DBV	5	3000	200.0	183.0	25.0
TLVH431AIDBVT	SOT-23	DBV	5	250	203.0	203.0	35.0
TLVH431AIDBVT	SOT-23	DBV	5	250	180.0	180.0	18.0
TLVH431AIDBZR	SOT-23	DBZ	3	3000	200.0	183.0	25.0
TLVH431AIDBZR	SOT-23	DBZ	3	3000	183.0	183.0	20.0
TLVH431AIDBZR	SOT-23	DBZ	3	3000	180.0	180.0	18.0
TLVH431AIDBZR	SOT-23	DBZ	3	3000	180.0	180.0	18.0
TLVH431AIDBZT	SOT-23	DBZ	3	250	180.0	180.0	18.0
TLVH431AIDBZTG4	SOT-23	DBZ	3	250	183.0	183.0	20.0

Device	Package Type	Package Drawing	Pins	SPQ	Length (mm)	Width (mm)	Height (mm)
TLVH431AIDCKR	SC70	DCK	6	3000	200.0	183.0	25.0
TLVH431AIDCKT	SC70	DCK	6	250	203.0	203.0	35.0
TLVH431AIPK	SOT-89	PK	3	1000	340.0	340.0	38.0
TLVH431AQDBVR	SOT-23	DBV	5	3000	203.0	203.0	35.0
TLVH431AQDBVR	SOT-23	DBV	5	3000	180.0	180.0	18.0
TLVH431AQDBVT	SOT-23	DBV	5	250	180.0	180.0	18.0
TLVH431AQDBVT	SOT-23	DBV	5	250	203.0	203.0	35.0
TLVH431AQDBZR	SOT-23	DBZ	3	3000	180.0	180.0	18.0
TLVH431AQDBZR	SOT-23	DBZ	3	3000	183.0	183.0	20.0
TLVH431AQDBZT	SOT-23	DBZ	3	250	180.0	180.0	18.0
TLVH431AQDBZTG4	SOT-23	DBZ	3	250	183.0	183.0	20.0
TLVH431AQDCKR	SC70	DCK	6	3000	200.0	183.0	25.0
TLVH431AQDCKT	SC70	DCK	6	250	200.0	183.0	25.0
TLVH431AQPK	SOT-89	PK	3	1000	340.0	340.0	38.0
TLVH431BCDBVR	SOT-23	DBV	5	3000	180.0	180.0	18.0
TLVH431BCDBVT	SOT-23	DBV	5	250	180.0	180.0	18.0
TLVH431BCDBVT	SOT-23	DBV	5	250	200.0	183.0	25.0
TLVH431BCDBZR	SOT-23	DBZ	3	3000	180.0	180.0	18.0
TLVH431BCDBZT	SOT-23	DBZ	3	250	180.0	180.0	18.0
TLVH431BCDBZTG4	SOT-23	DBZ	3	250	183.0	183.0	20.0
TLVH431BCDCKR	SC70	DCK	6	3000	200.0	183.0	25.0
TLVH431BCDCKT	SC70	DCK	6	250	200.0	183.0	25.0
TLVH431BCPK	SOT-89	PK	3	1000	340.0	340.0	38.0
TLVH431BIDBVR	SOT-23	DBV	5	3000	200.0	183.0	25.0
TLVH431BIDBVR	SOT-23	DBV	5	3000	180.0	180.0	18.0
TLVH431BIDBVT	SOT-23	DBV	5	250	203.0	203.0	35.0
TLVH431BIDBVT	SOT-23	DBV	5	250	180.0	180.0	18.0
TLVH431BIDBZR	SOT-23	DBZ	3	3000	180.0	180.0	18.0
TLVH431BIDBZR	SOT-23	DBZ	3	3000	180.0	180.0	18.0
TLVH431BIDBZR	SOT-23	DBZ	3	3000	183.0	183.0	20.0
TLVH431BIDBZR	SOT-23	DBZ	3	3000	200.0	183.0	25.0
TLVH431BIDBZRG4	SOT-23	DBZ	3	3000	183.0	183.0	20.0
TLVH431BIDBZT	SOT-23	DBZ	3	250	180.0	180.0	18.0
TLVH431BIDCKR	SC70	DCK	6	3000	200.0	183.0	25.0
TLVH431BIDCKT	SC70	DCK	6	250	203.0	203.0	35.0
TLVH431BIPK	SOT-89	PK	3	1000	340.0	340.0	38.0
TLVH431BQDBVR	SOT-23	DBV	5	3000	180.0	180.0	18.0
TLVH431BQDBVR	SOT-23	DBV	5	3000	200.0	183.0	25.0
TLVH431BQDBVT	SOT-23	DBV	5	250	203.0	203.0	35.0
TLVH431BQDBVT	SOT-23	DBV	5	250	180.0	180.0	18.0
TLVH431BQDBZR	SOT-23	DBZ	3	3000	183.0	183.0	20.0
TLVH431BQDBZR	SOT-23	DBZ	3	3000	180.0	180.0	18.0
TLVH431BQDBZRG4	SOT-23	DBZ	3	3000	183.0	183.0	20.0

Device	Package Type	Package Drawing	Pins	SPQ	Length (mm)	Width (mm)	Height (mm)
TLVH431BQDBZT	SOT-23	DBZ	3	250	180.0	180.0	18.0
TLVH431BQDCKR	SC70	DCK	6	3000	200.0	183.0	25.0
TLVH431BQDCKT	SC70	DCK	6	250	203.0	203.0	35.0
TLVH431BQPK	SOT-89	PK	3	1000	340.0	340.0	38.0
TLVH431CDBVR	SOT-23	DBV	5	3000	180.0	180.0	18.0
TLVH431CDBVR	SOT-23	DBV	5	3000	203.0	203.0	35.0
TLVH431CDBVT	SOT-23	DBV	5	250	203.0	203.0	35.0
TLVH431CDBVT	SOT-23	DBV	5	250	180.0	180.0	18.0
TLVH431CDBZR	SOT-23	DBZ	3	3000	180.0	180.0	18.0
TLVH431CDBZT	SOT-23	DBZ	3	250	180.0	180.0	18.0
TLVH431CDBZTG4	SOT-23	DBZ	3	250	183.0	183.0	20.0
TLVH431CDCKT	SC70	DCK	6	250	200.0	183.0	25.0
TLVH431CPK	SOT-89	PK	3	1000	340.0	340.0	38.0
TLVH431IDBVR	SOT-23	DBV	5	3000	180.0	180.0	18.0
TLVH431IDBVR	SOT-23	DBV	5	3000	200.0	183.0	25.0
TLVH431IDBVT	SOT-23	DBV	5	250	203.0	203.0	35.0
TLVH431IDBVT	SOT-23	DBV	5	250	180.0	180.0	18.0
TLVH431IDBZR	SOT-23	DBZ	3	3000	180.0	180.0	18.0
TLVH431IDBZR	SOT-23	DBZ	3	3000	183.0	183.0	20.0
TLVH431IDBZT	SOT-23	DBZ	3	250	180.0	180.0	18.0
TLVH431IDCKR	SC70	DCK	6	3000	203.0	203.0	35.0
TLVH431IDCKT	SC70	DCK	6	250	203.0	203.0	35.0
TLVH431IPK	SOT-89	PK	3	1000	340.0	340.0	38.0
TLVH431QDBVR	SOT-23	DBV	5	3000	200.0	183.0	25.0
TLVH431QDBVR	SOT-23	DBV	5	3000	180.0	180.0	18.0
TLVH431QDBVT	SOT-23	DBV	5	250	180.0	180.0	18.0
TLVH431QDBVT	SOT-23	DBV	5	250	203.0	203.0	35.0
TLVH431QDBZR	SOT-23	DBZ	3	3000	180.0	180.0	18.0
TLVH431QDBZT	SOT-23	DBZ	3	250	180.0	180.0	18.0
TLVH431QDBZTG4	SOT-23	DBZ	3	250	183.0	183.0	20.0
TLVH431QDCKR	SC70	DCK	6	3000	200.0	183.0	25.0
TLVH431QDCKT	SC70	DCK	6	250	200.0	183.0	25.0
TLVH431QPK	SOT-89	PK	3	1000	340.0	340.0	38.0
TLVH432ACDBZR	SOT-23	DBZ	3	3000	180.0	180.0	18.0
TLVH432ACDBZRG4	SOT-23	DBZ	3	3000	183.0	183.0	20.0
TLVH432ACDBZT	SOT-23	DBZ	3	250	180.0	180.0	18.0
TLVH432AIDBZR	SOT-23	DBZ	3	3000	180.0	180.0	18.0
TLVH432AIPK	SOT-89	PK	3	1000	340.0	340.0	38.0
TLVH432AQDBZR	SOT-23	DBZ	3	3000	180.0	180.0	18.0
TLVH432AQDBZT	SOT-23	DBZ	3	250	180.0	180.0	18.0
TLVH432BCDBZR	SOT-23	DBZ	3	3000	180.0	180.0	18.0
TLVH432BCDBZRG4	SOT-23	DBZ	3	3000	183.0	183.0	20.0
TLVH432BCPK	SOT-89	PK	3	1000	340.0	340.0	38.0

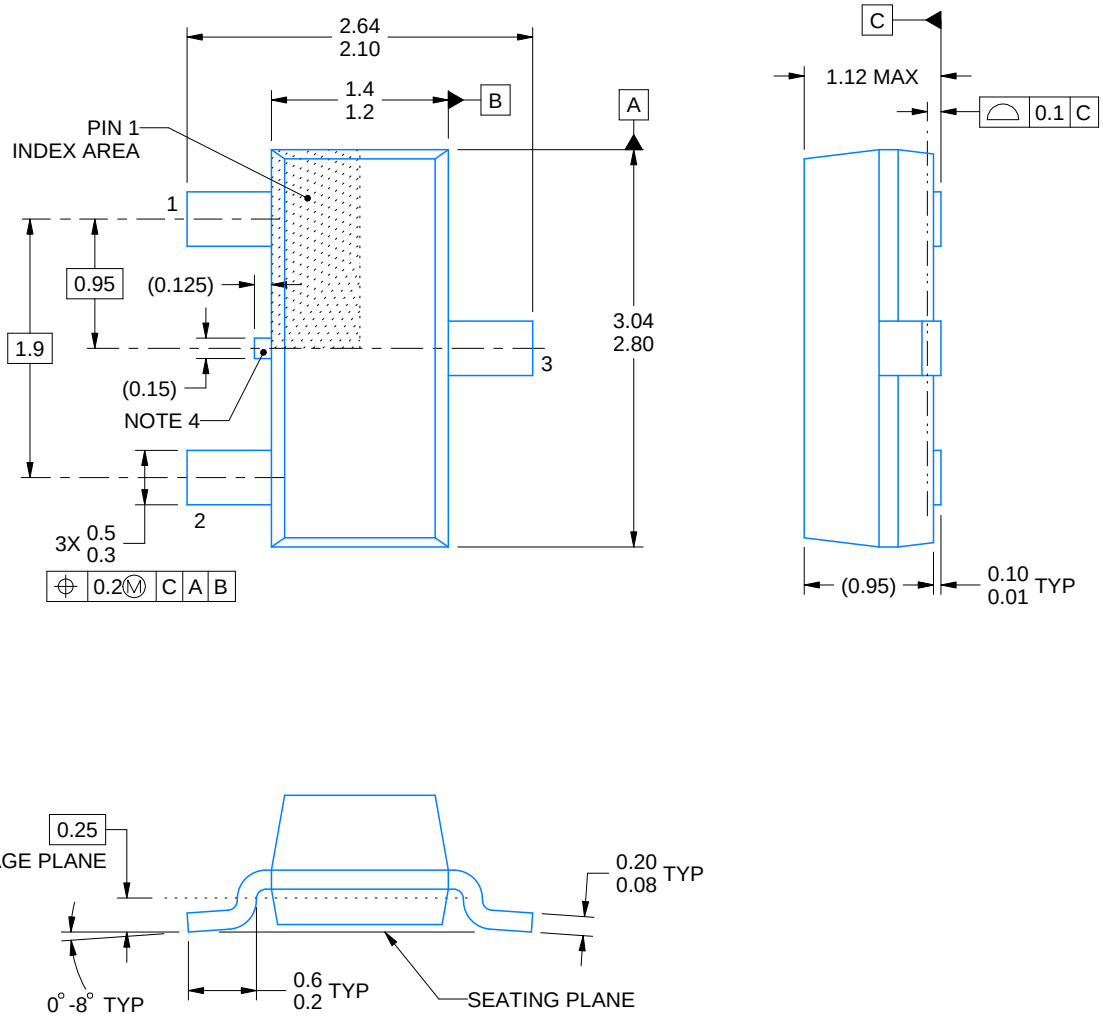
Device	Package Type	Package Drawing	Pins	SPQ	Length (mm)	Width (mm)	Height (mm)
TLVH432BIDBZR	SOT-23	DBZ	3	3000	180.0	180.0	18.0
TLVH432BQDBZR	SOT-23	DBZ	3	3000	180.0	180.0	18.0
TLVH432BQDBZT	SOT-23	DBZ	3	250	180.0	180.0	18.0
TLVH432BQDBZTG4	SOT-23	DBZ	3	250	183.0	183.0	20.0
TLVH432CDBZR	SOT-23	DBZ	3	3000	180.0	180.0	18.0
TLVH432CDBZT	SOT-23	DBZ	3	250	180.0	180.0	18.0
TLVH432CPK	SOT-89	PK	3	1000	340.0	340.0	38.0
TLVH432IDBZR	SOT-23	DBZ	3	3000	180.0	180.0	18.0
TLVH432QDBZR	SOT-23	DBZ	3	3000	180.0	180.0	18.0
TLVH432QDBZT	SOT-23	DBZ	3	250	180.0	180.0	18.0
TLVH432QPK	SOT-89	PK	3	1000	340.0	340.0	38.0

PACKAGE OUTLINE

DBZ0003A

SOT-23 - 1.12 mm max height

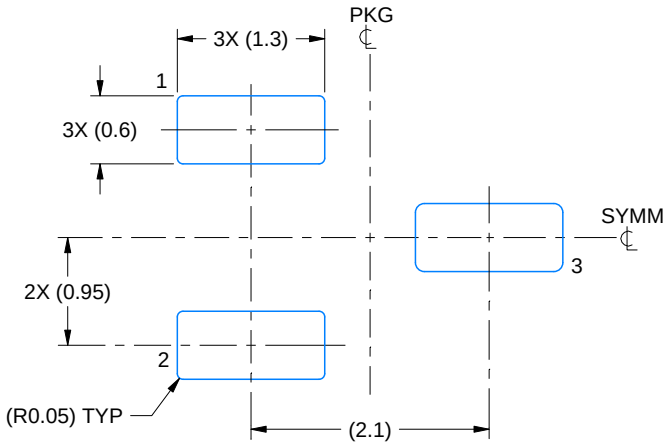
SMALL OUTLINE TRANSISTOR



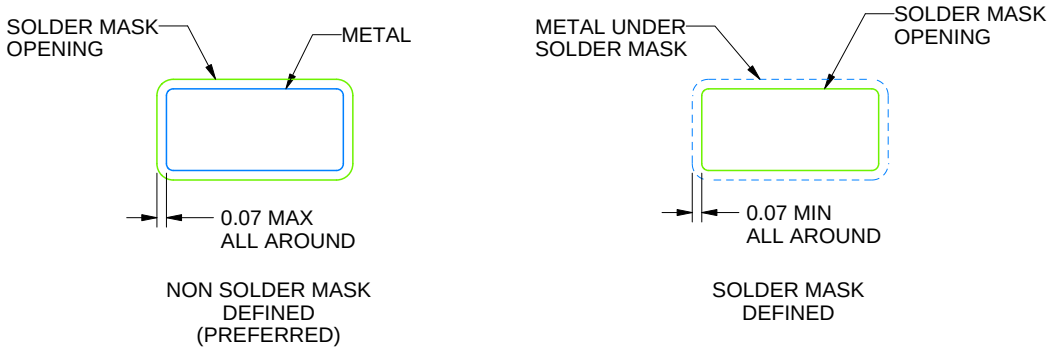
4214838/D 03/2023

NOTES:

1. All linear dimensions are in millimeters. Any dimensions in parenthesis are for reference only. Dimensioning and tolerancing per ASME Y14.5M.
2. This drawing is subject to change without notice.
3. Reference JEDEC registration TO-236, except minimum foot length.
4. Support pin may differ or may not be present.



LAND PATTERN EXAMPLE
SCALE:15X

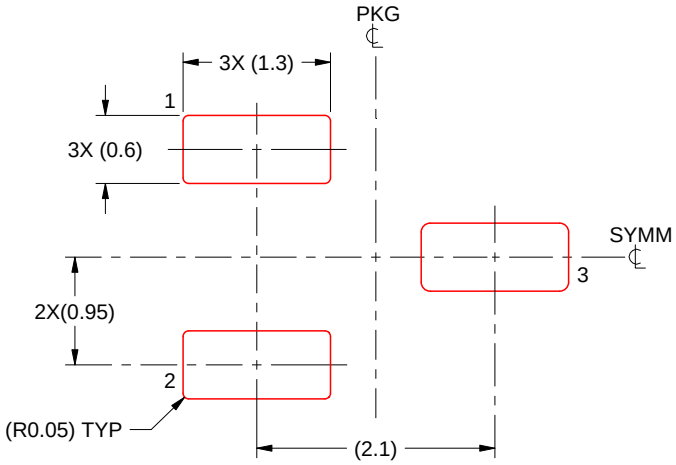


SOLDER MASK DETAILS

4214838/D 03/2023

NOTES: (continued)

4. Publication IPC-7351 may have alternate designs.
5. Solder mask tolerances between and around signal pads can vary based on board fabrication site.



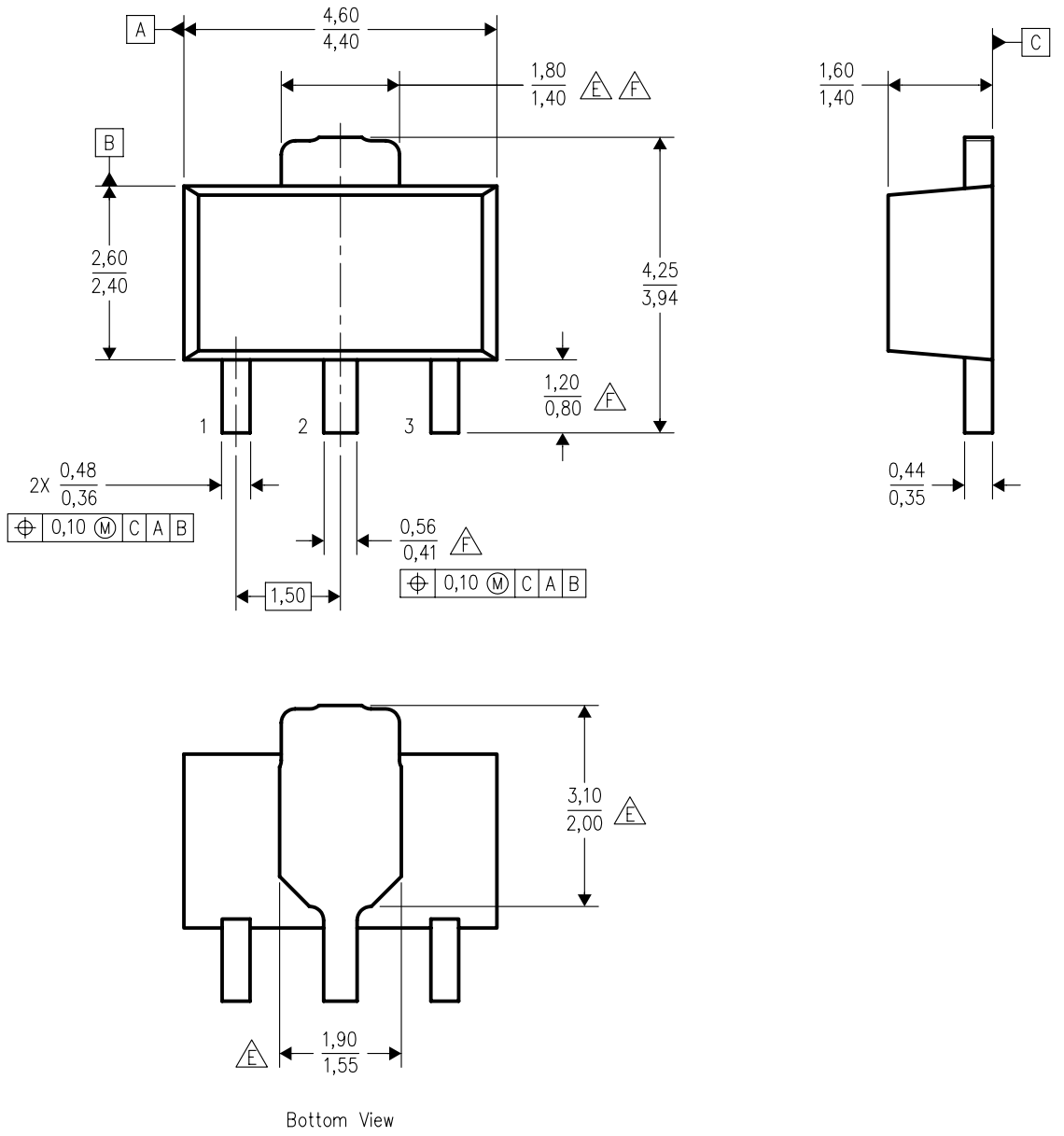
SOLDER PASTE EXAMPLE
BASED ON 0.125 THICK STENCIL
SCALE:15X

4214838/D 03/2023

NOTES: (continued)

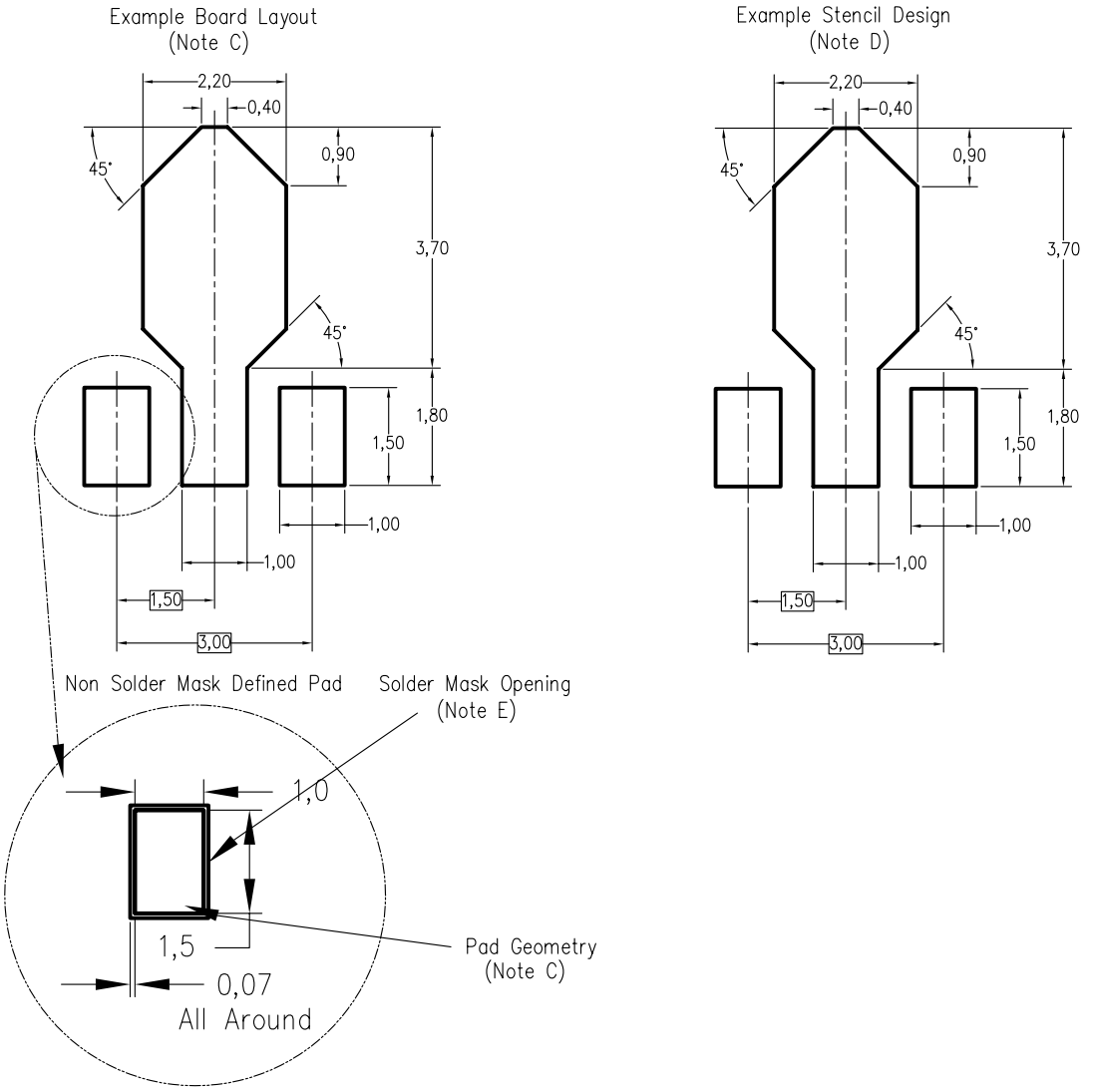
6. Laser cutting apertures with trapezoidal walls and rounded corners may offer better paste release. IPC-7525 may have alternate design recommendations.
7. Board assembly site may have different recommendations for stencil design.

PK (R–PSSO–F3) PLASTIC SINGLE–IN–LINE PACKAGE



NOTES:
A. All linear dimensions are in millimeters. Dimensioning and tolerancing per ASME Y14.5M–1994.
B. This drawing is subject to change without notice.
C. The center lead is in electrical contact with the tab.
D. Body dimensions do not include mold flash or protrusion. Mold flash and protrusion not to exceed 0.15 per side.
E. Thermal pad contour optional within these dimensions.
F. Falls within JEDEC TO–243 variation AA, except minimum lead length, pin 2 minimum lead width, minimum tab width.

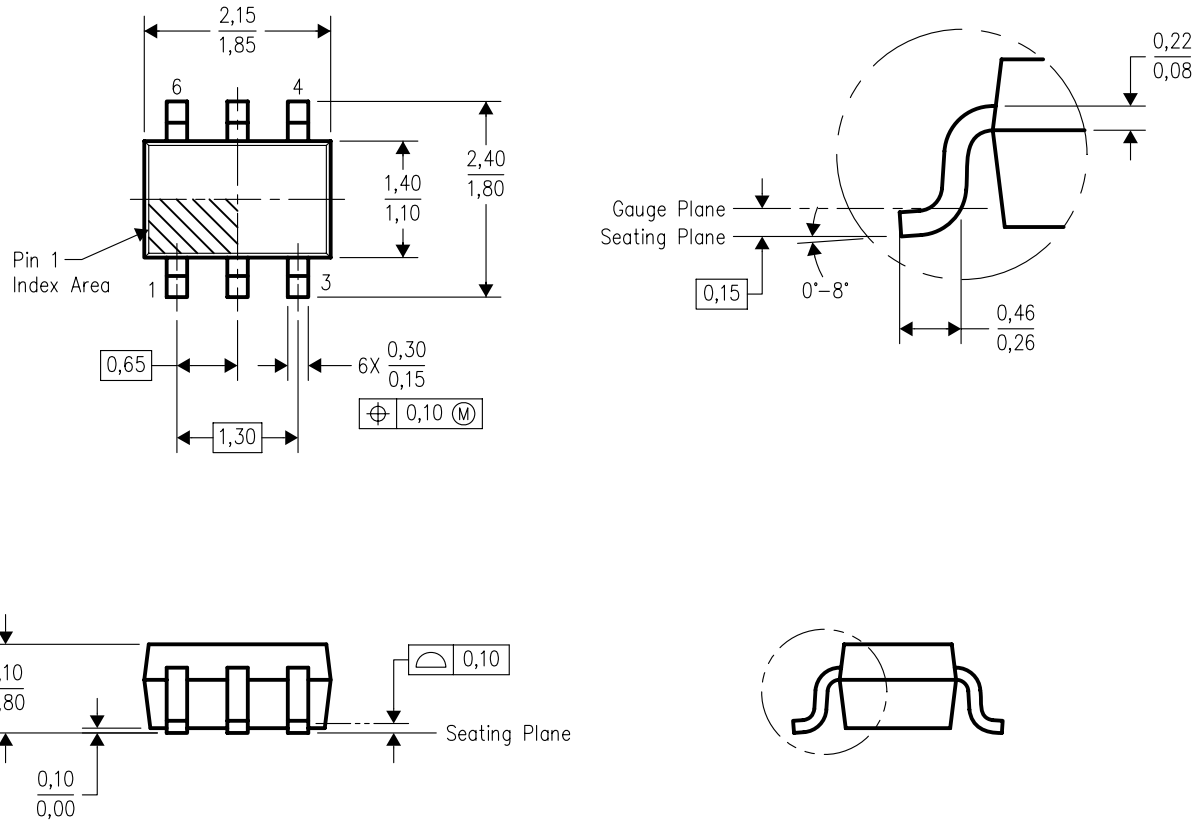
PK (R–PDSO–G3)



4208221/A 09/06

NOTES:
A. All linear dimensions are in millimeters.
B. This drawing is subject to change without notice.
C. Publication IPC–7351 is recommended for alternate designs.
D. Laser cutting apertures with trapezoidal walls and also rounding corners will offer better paste release. Customers should contact their board assembly site for stencil design recommendations. Refer to IPC–7525.
E. Customers should contact their board fabrication site for solder mask tolerances between and around signal pads.

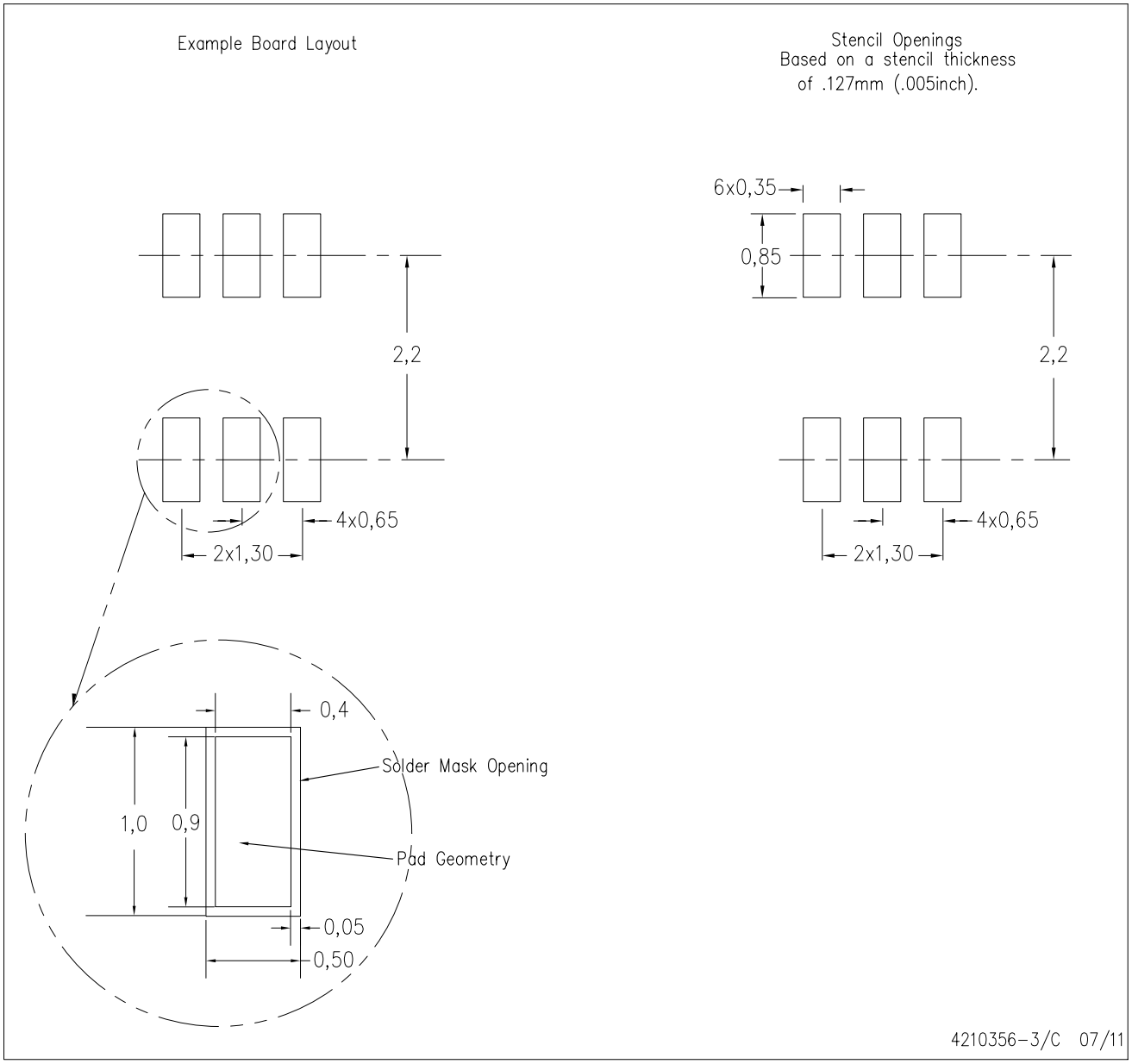
DCK (R–PDSO–G6) PLASTIC SMALL–OUTLINE PACKAGE



4093553–4/G 01/2007

NOTES:
A. All linear dimensions are in millimeters.
B. This drawing is subject to change without notice.
C. Body dimensions do not include mold flash or protrusion. Mold flash and protrusion shall not exceed 0.15 per side.
D. Falls within JEDEC MO–203 variation AB.

DCK (R–PDSO–G6) PLASTIC SMALL OUTLINE



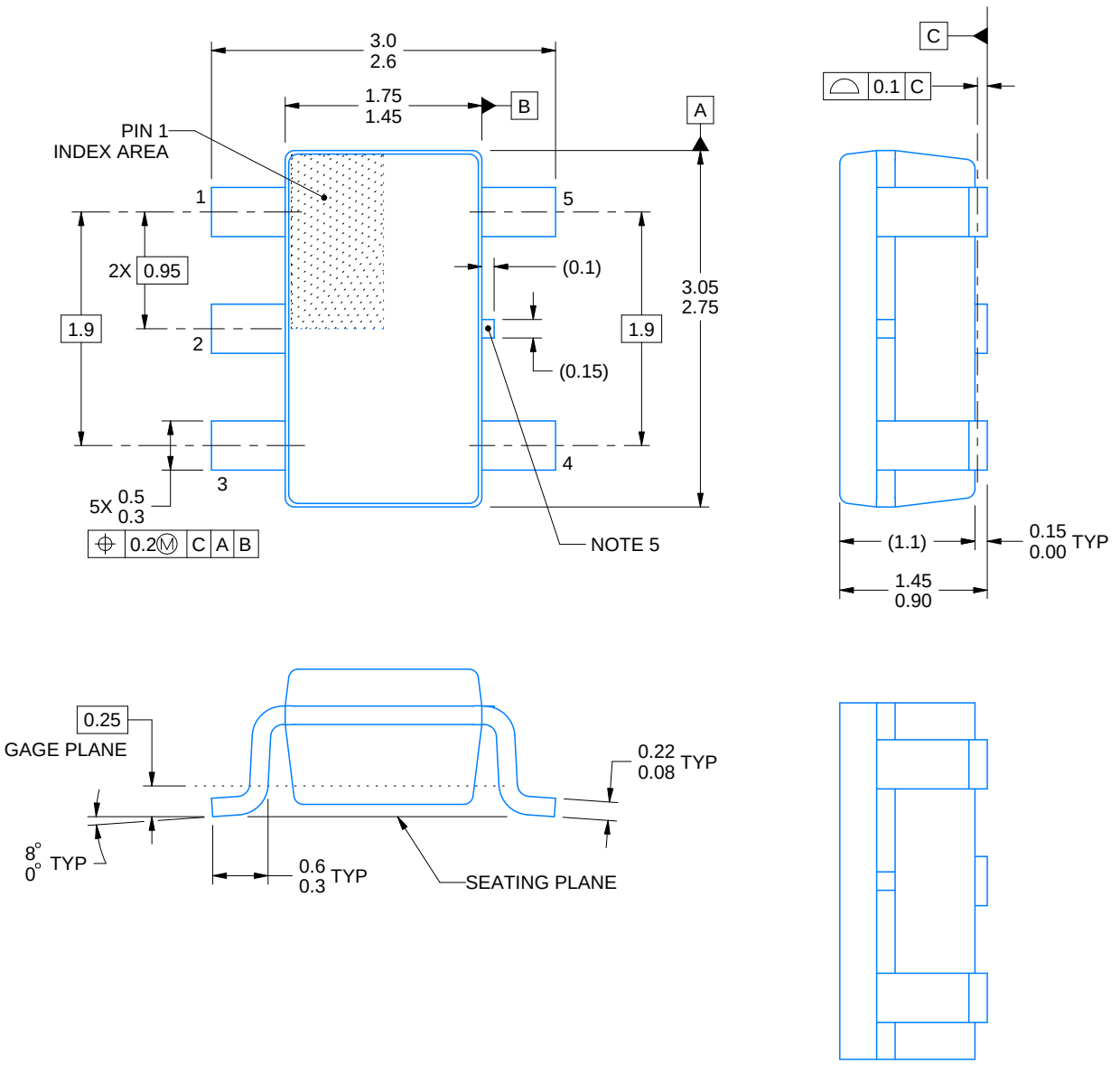
NOTES:
A. All linear dimensions are in millimeters.
B. This drawing is subject to change without notice.
C. Customers should place a note on the circuit board fabrication drawing not to alter the center solder mask defined pad.
D. Publication IPC–7351 is recommended for alternate designs.
E. Laser cutting apertures with trapezoidal walls and also rounding corners will offer better paste release. Customers should contact their board assembly site for stencil design recommendations. Example stencil design based on a 50% volumetric metal load solder paste. Refer to IPC–7525 for other stencil recommendations.

PACKAGE OUTLINE

DBV0005A

SOT-23 - 1.45 mm max height

SMALL OUTLINE TRANSISTOR

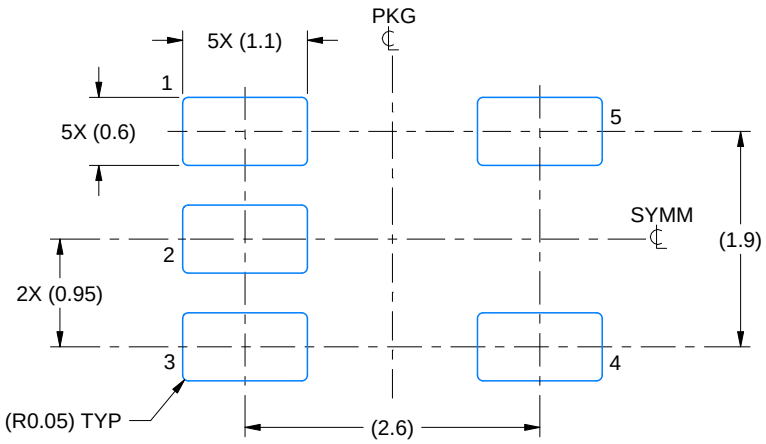


ALTERNATIVE PACKAGE SINGULATION VIEW

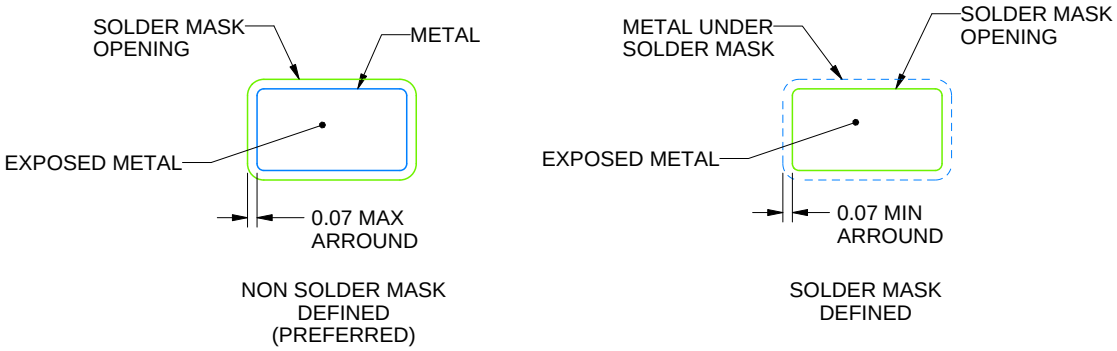
4214839/J 02/2024

NOTES:

1. All linear dimensions are in millimeters. Any dimensions in parenthesis are for reference only. Dimensioning and tolerancing per ASME Y14.5M.
2. This drawing is subject to change without notice.
3. Refernce JEDEC MO-178.
4. Body dimensions do not include mold flash, protrusions, or gate burrs. Mold flash, protrusions, or gate burrs shall not exceed 0.25 mm per side.
5. Support pin may differ or may not be present.



LAND PATTERN EXAMPLE
EXPOSED METAL SHOWN
SCALE:15X

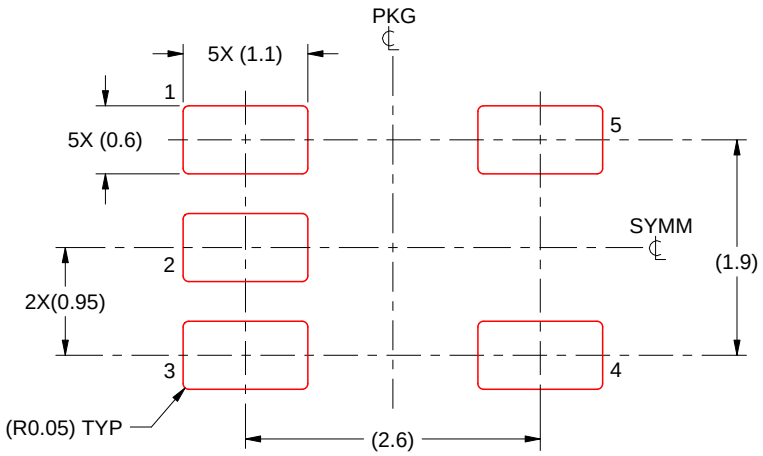


SOLDER MASK DETAILS

4214839/J 02/2024

NOTES: (continued)

6. Publication IPC-7351 may have alternate designs.
7. Solder mask tolerances between and around signal pads can vary based on board fabrication site.



SOLDER PASTE EXAMPLE
BASED ON 0.125 mm THICK STENCIL
SCALE:15X

4214839/J 02/2024

NOTES: (continued)

8. Laser cutting apertures with trapezoidal walls and rounded corners may offer better paste release. IPC-7525 may have alternate design recommendations.
9. Board assembly site may have different recommendations for stencil design.

LP 3 TO-92 - 5.34 mm max height

TRANSISTOR OUTLINE

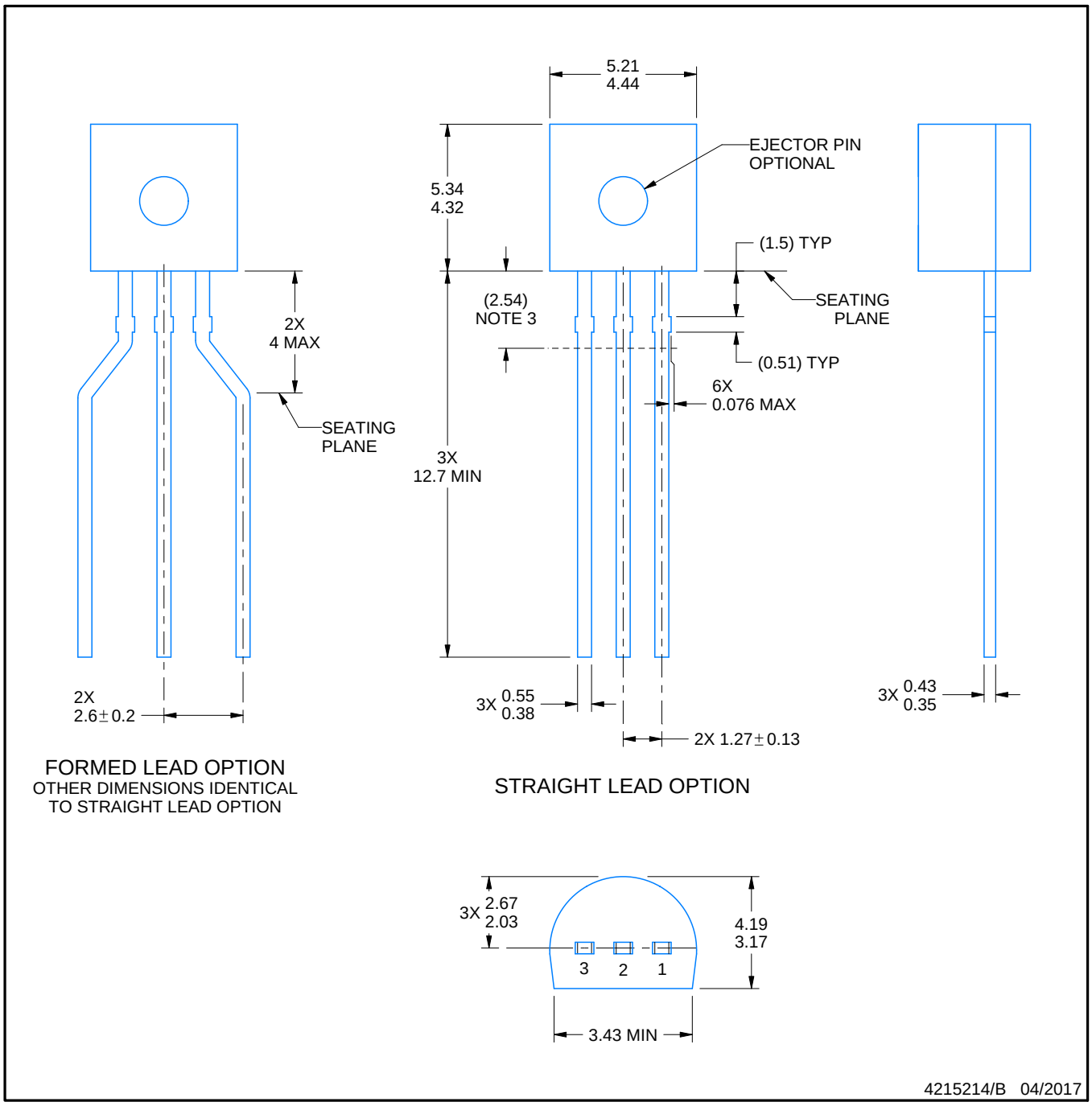
Images above are just a representation of the package family, actual package may vary.
Refer to the product data sheet for package details.

LP0003A

PACKAGE OUTLINE

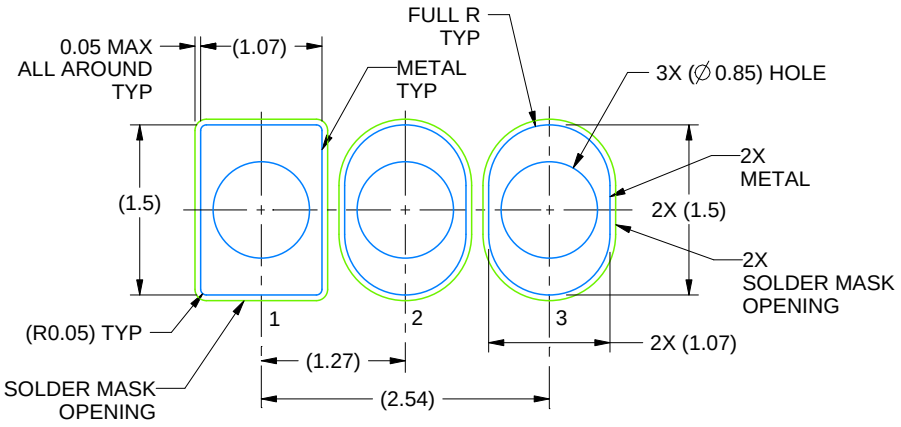
TO-92 - 5.34 mm max height

TO-92

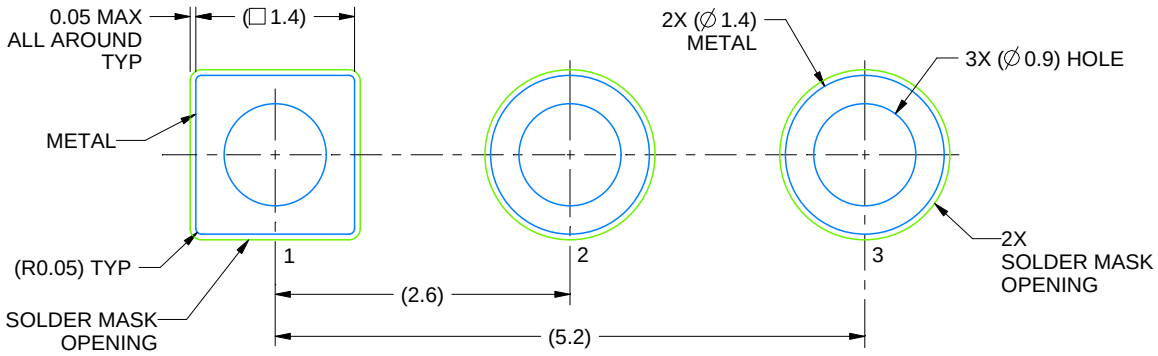


NOTES:

1. All linear dimensions are in millimeters. Any dimensions in parenthesis are for reference only. Dimensioning and tolerancing per ASME Y14.5M.
2. This drawing is subject to change without notice.
3. Lead dimensions are not controlled within this area.
4. Reference JEDEC TO-226, variation AA.
5. Shipping method:
   a. Straight lead option available in bulk pack only.
   b. Formed lead option available in tape and reel or ammo pack.
   c. Specific products can be offered in limited combinations of shipping medium and lead options.
   d. Consult product folder for more information on available options.



LAND PATTERN EXAMPLE
STRAIGHT LEAD OPTION
NON-SOLDER MASK DEFINED
SCALE:15X



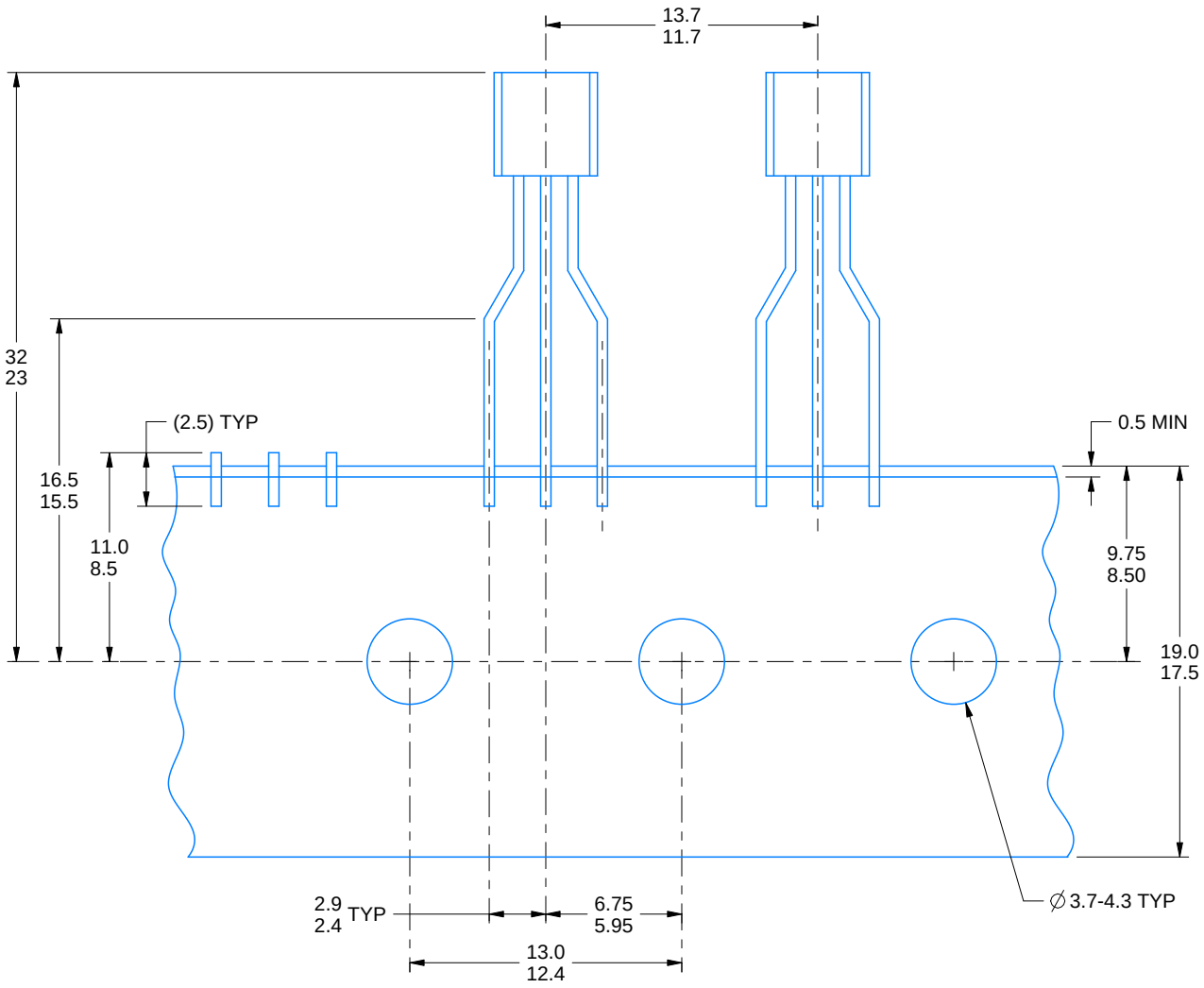
LAND PATTERN EXAMPLE
FORMED LEAD OPTION
NON-SOLDER MASK DEFINED
SCALE:15X

4215214/B 04/2017

LP0003A

TO-92 - 5.34 mm max height

TO-92



FOR FORMED LEAD OPTION PACKAGE

4215214/B 04/2017

重要なお知らせと免責事項

TI は、技術データと信頼性データ (データシートを含みます)、設計リソース (リファレンス· デザインを含みます)、アプリケーションや設計に関する各種アドバイス、Web ツール、安全性情報、その他のリソースを、欠陥が存在する可能性のある「現状のまま」提供しており、商品性および特定目的に対する適合性の黙示保証、第三者の知的財産権の非侵害保証を含むいかなる保証も、明示的または黙示的にかかわらず拒否します。

これらのリソースは、TI 製品を使用する設計の経験を積んだ開発者への提供を意図したものです。(1) お客様のアプリケーションに適した TI 製品の選定、(2) お客様のアプリケーションの設計、検証、試験、(3) お客様のアプリケーションに該当する各種規格や、その他のあらゆる安全性、セキュリティ、規制、または他の要件への確実な適合に関する責任を、お客様のみが単独で負うものとします。

上記の各種リソースは、予告なく変更される可能性があります。これらのリソースは、リソースで説明されている TI 製品を使用するアプリケーションの開発の目的でのみ、TI はその使用をお客様に許諾します。これらのリソースに関して、他の目的で複製することや掲載することは禁止されています。TI や第三者の知的財産権のライセンスが付与されている訳ではありません。お客様は、これらのリソースを自身で使用した結果発生するあらゆる申し立て、損害、費用、損失、責任について、TI およびその代理人を完全に補償するものとし、TI は一切の責任を拒否します。

TI の製品は、TI の販売条件、または ti.com やかかる TI 製品の関連資料などのいずれかを通じて提供する適用可能な条項の下で提供されています。TI がこれらのリソースを提供することは、適用される TI の保証または他の保証の放棄の拡大や変更を意味するものではありません。

お客様がいかなる追加条項または代替条項を提案した場合でも、TI はそれらに異議を唱え、拒否します。

郵送先住所：Texas Instruments, Post Office Box 655303, Dallas, Texas 75265
Copyright © 2024, Texas Instruments Incorporated